

10/531713

FIG.1

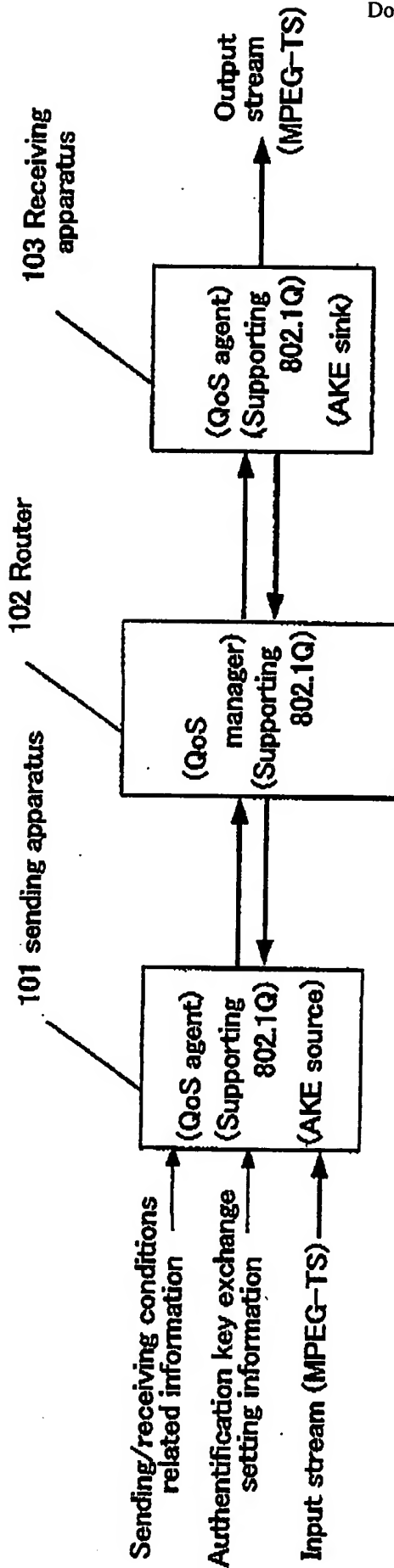
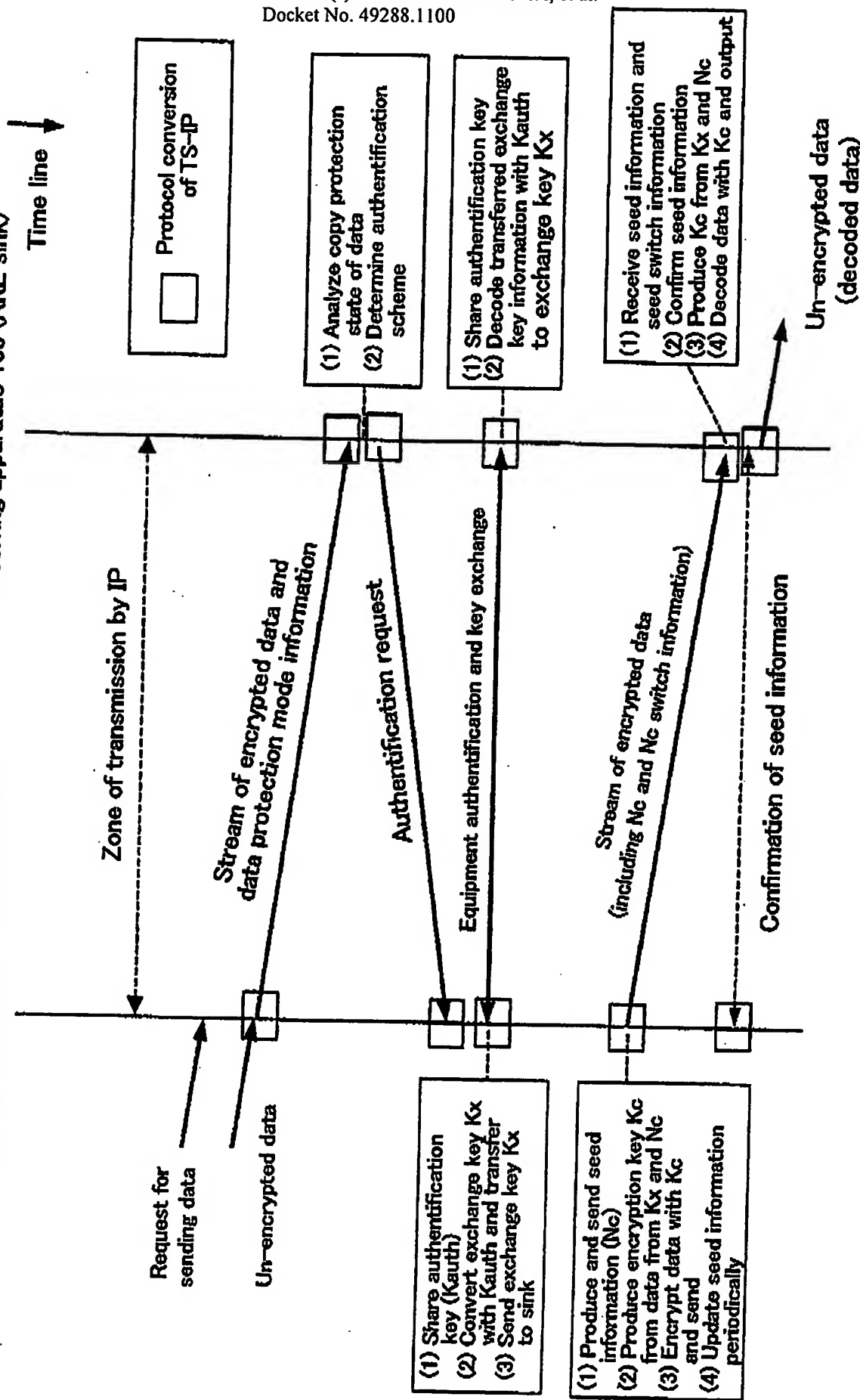


FIG.2 Sending apparatus 101 (AKE source) Receiving apparatus 103 (AKE sink)



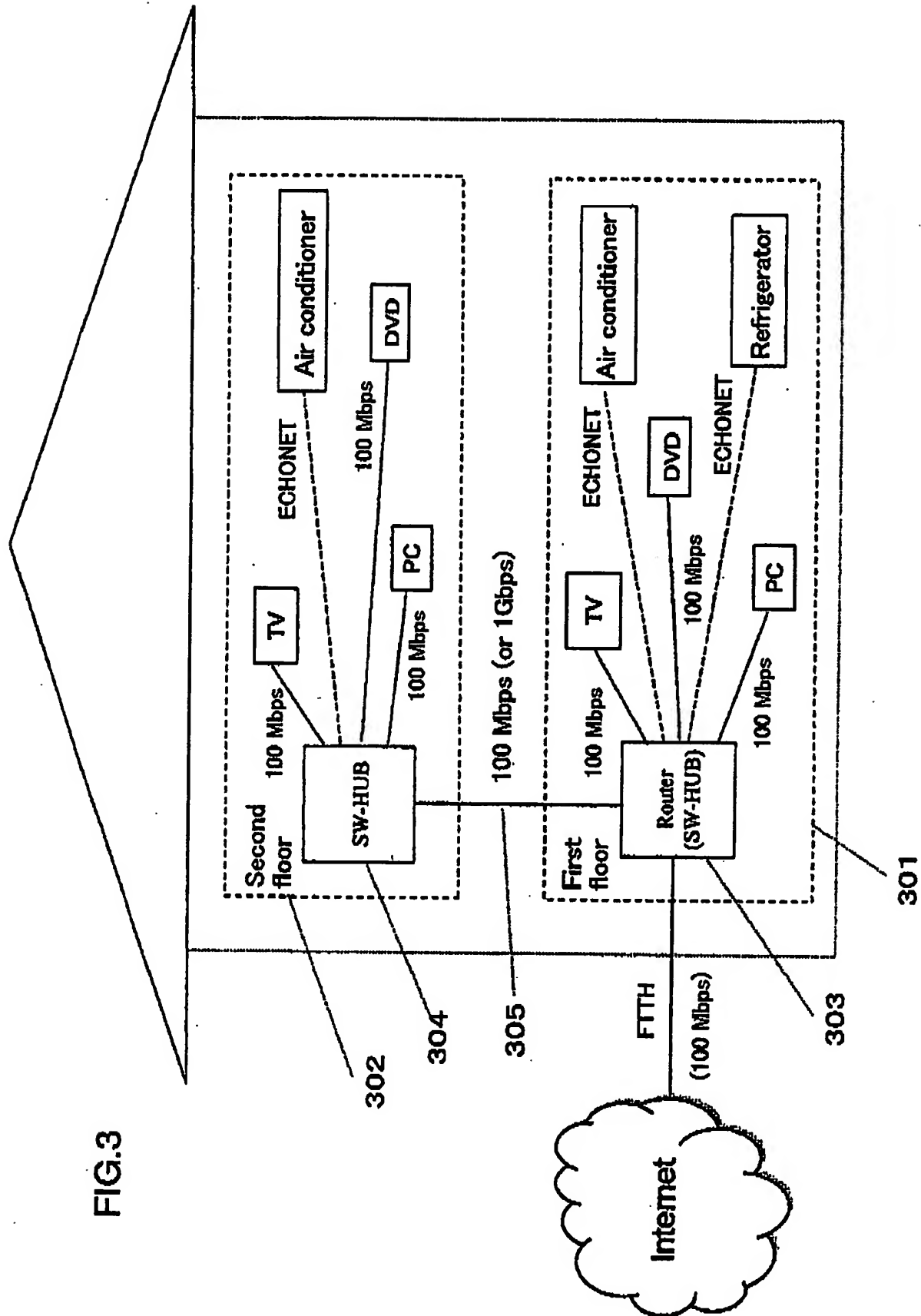
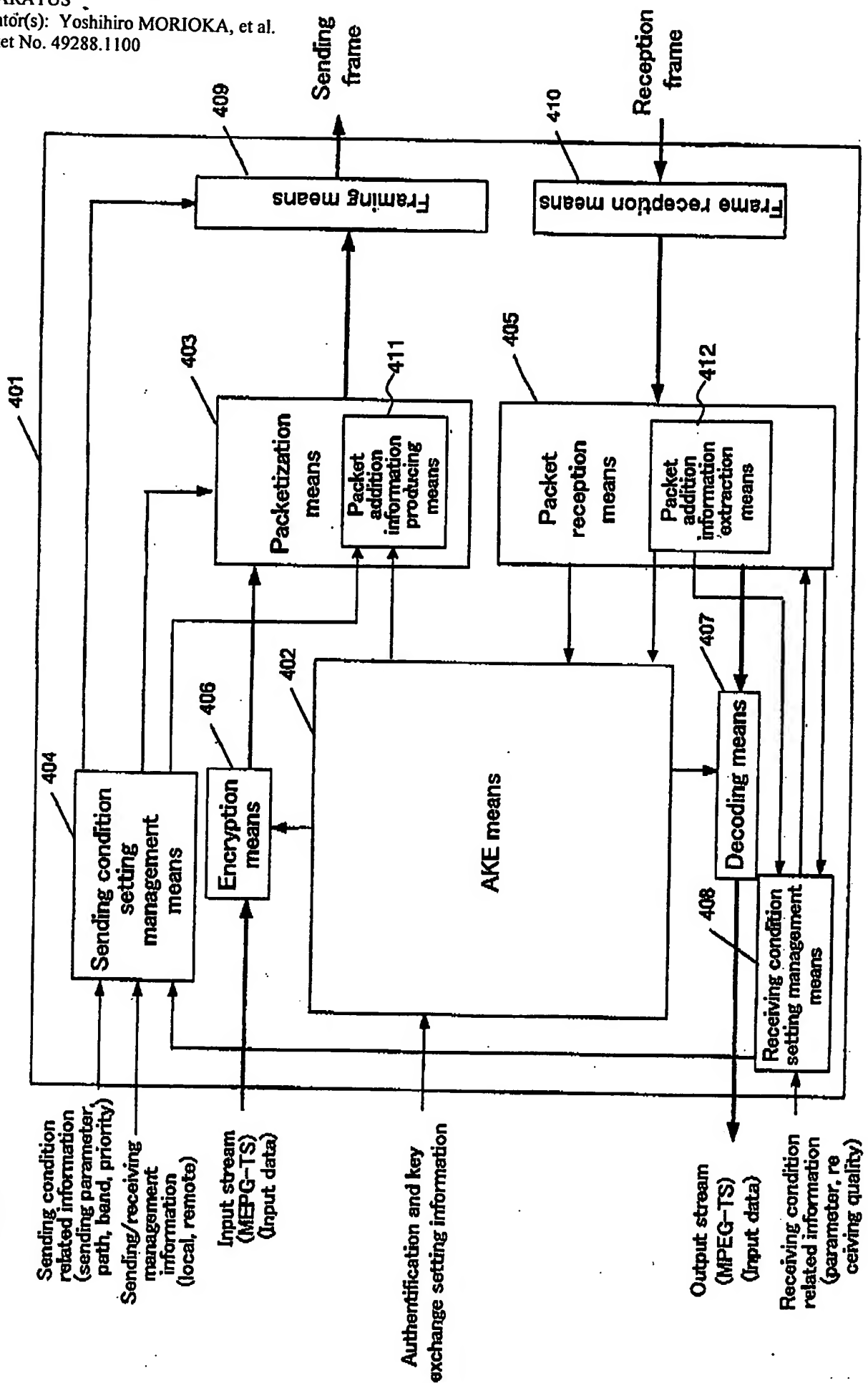
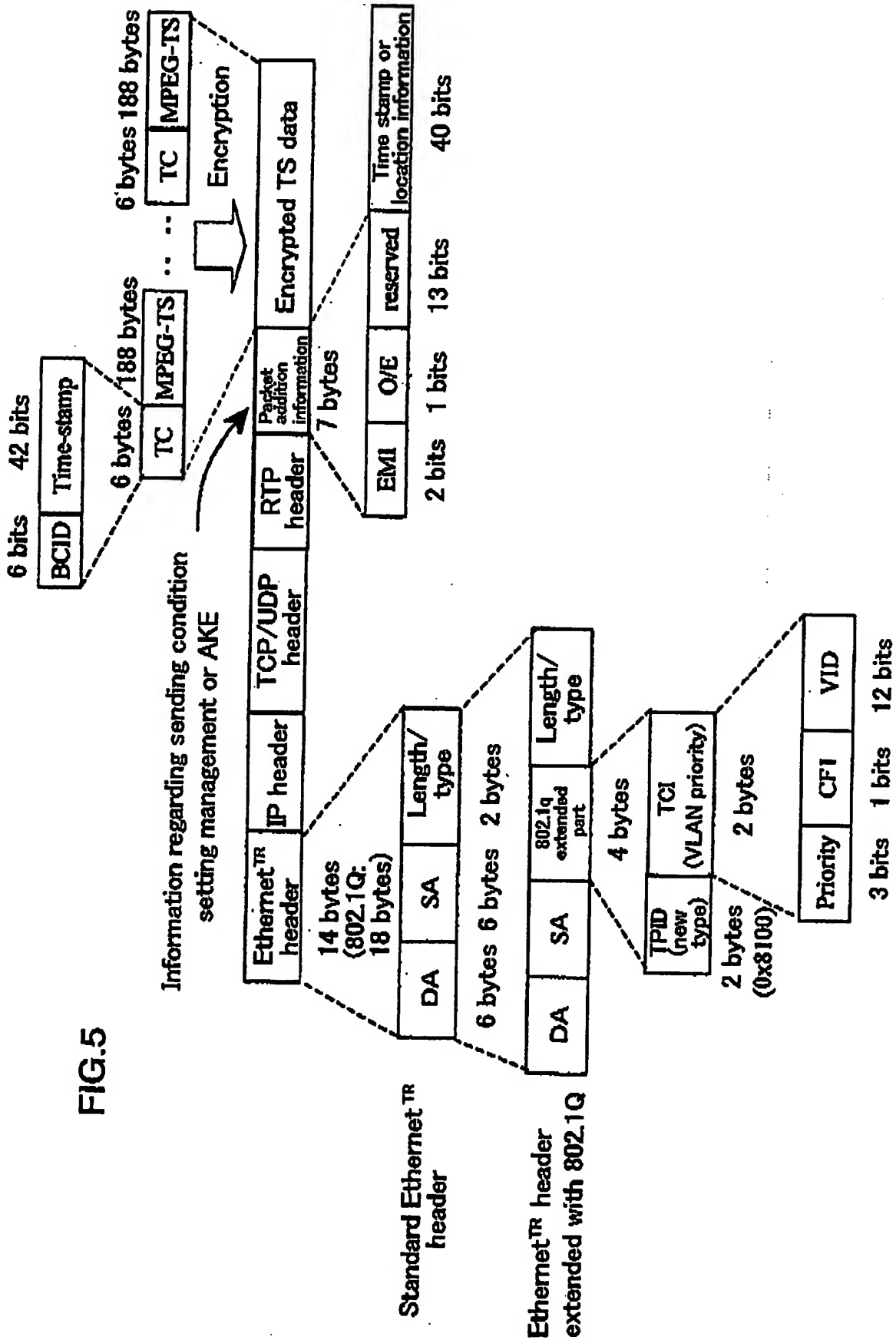


FIG.4





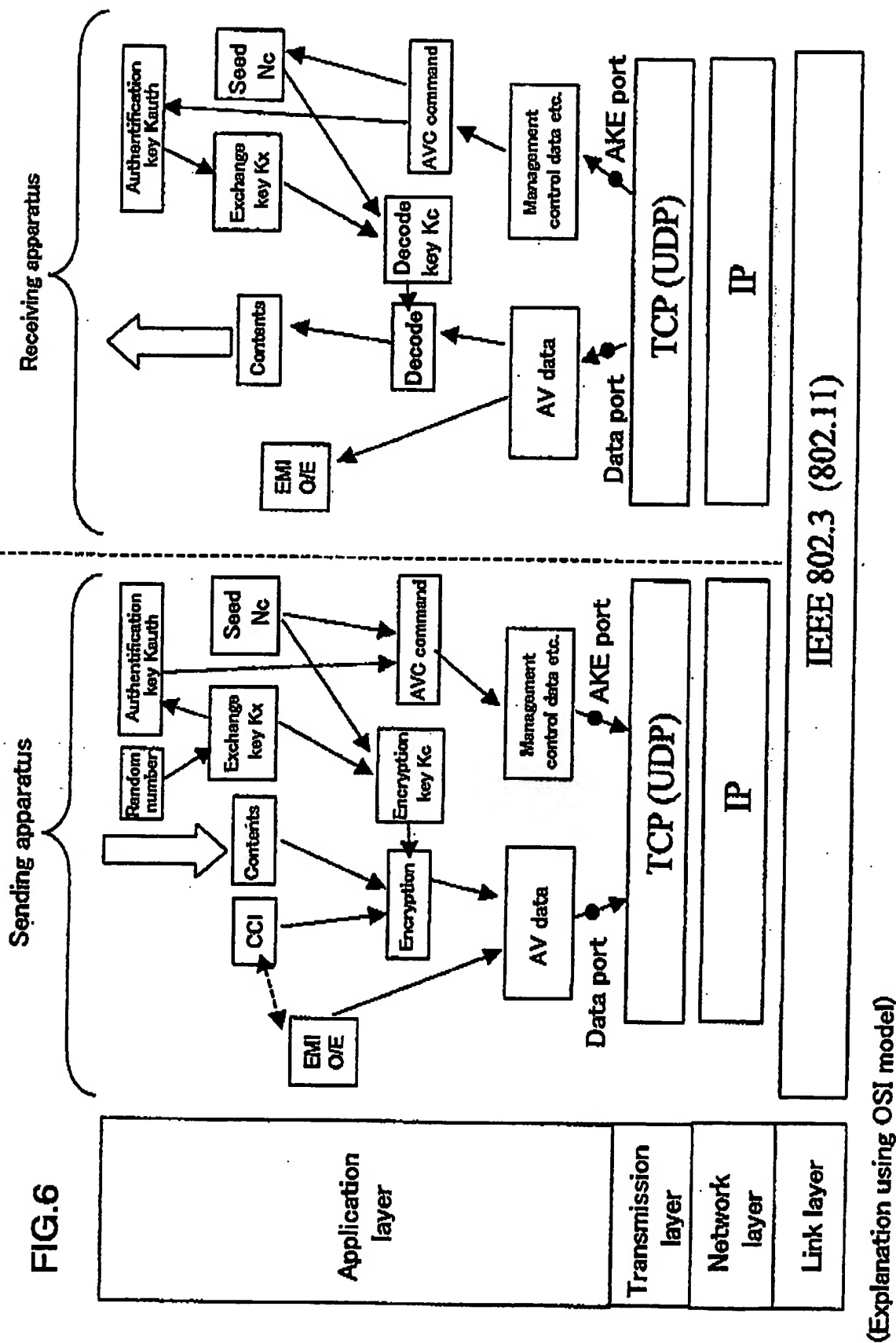


FIG.7

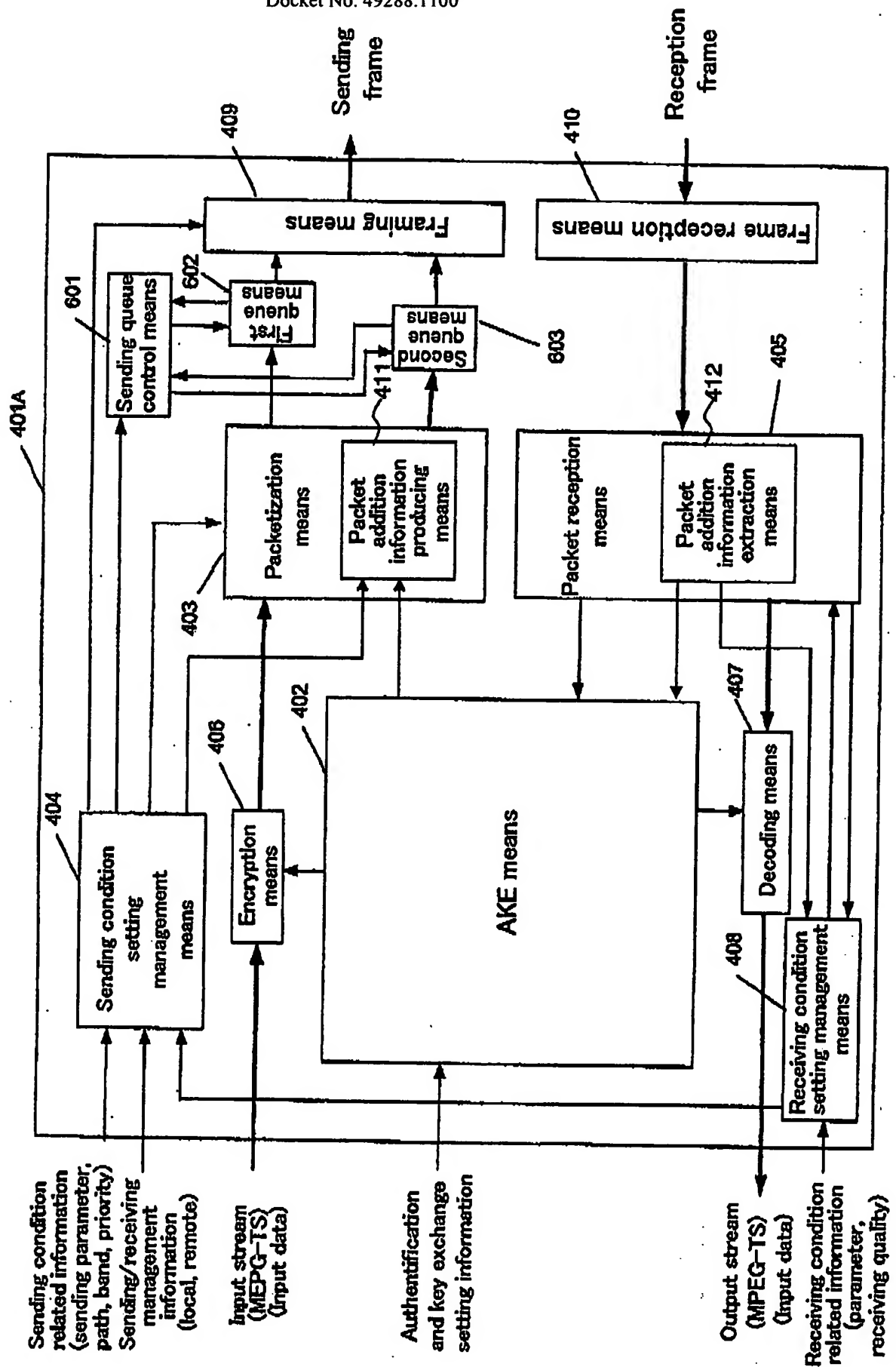
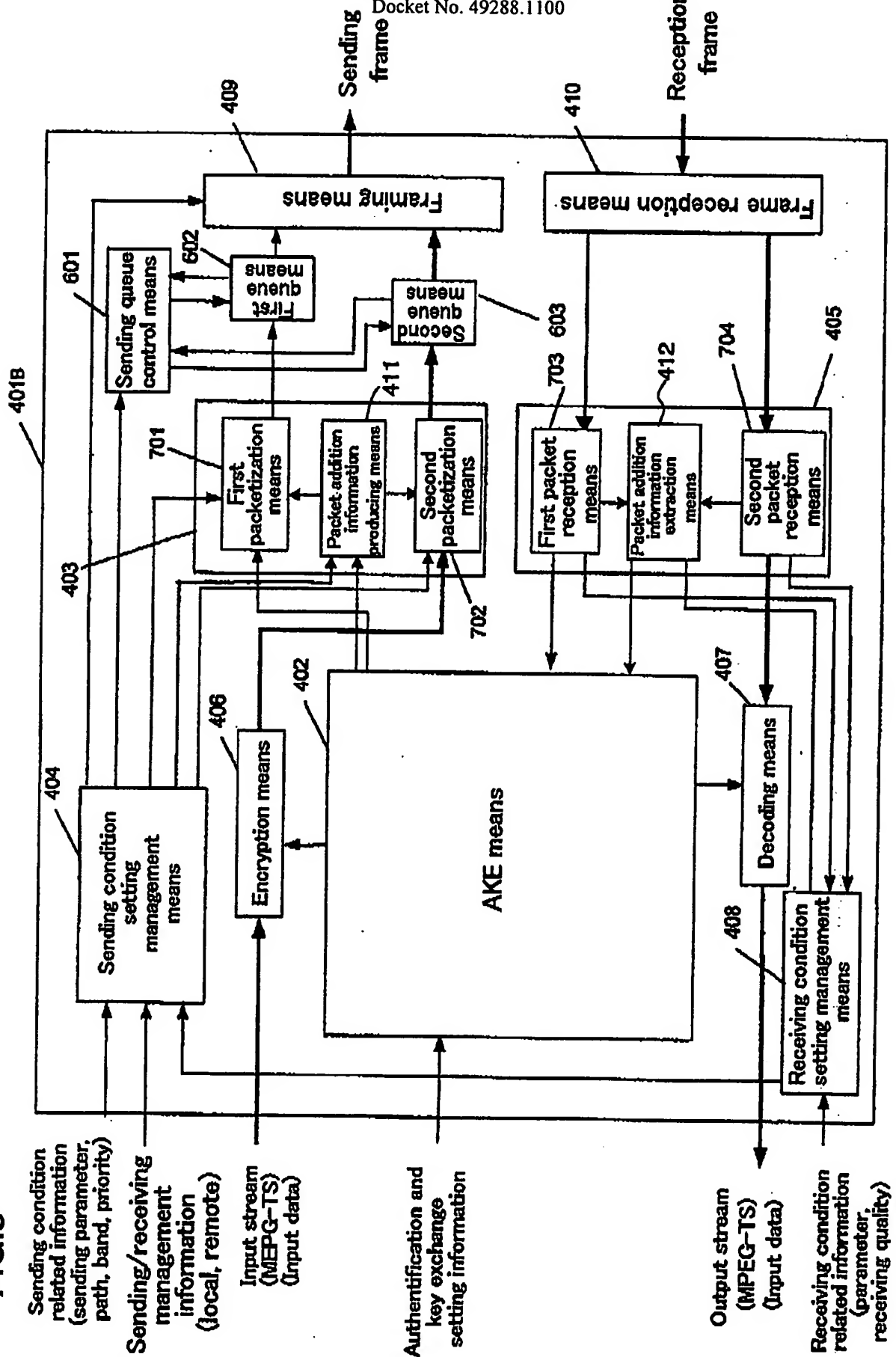
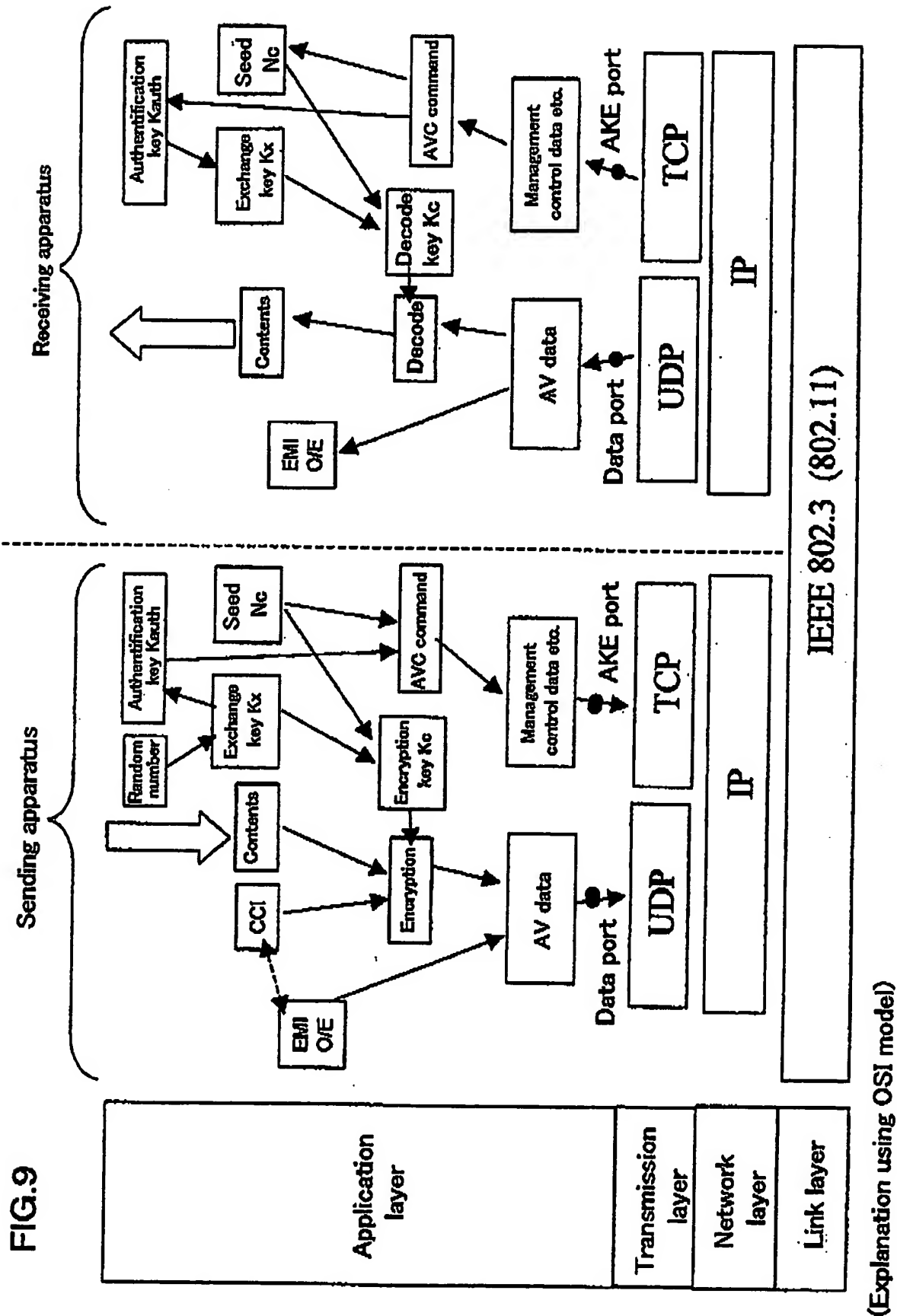


FIG.8







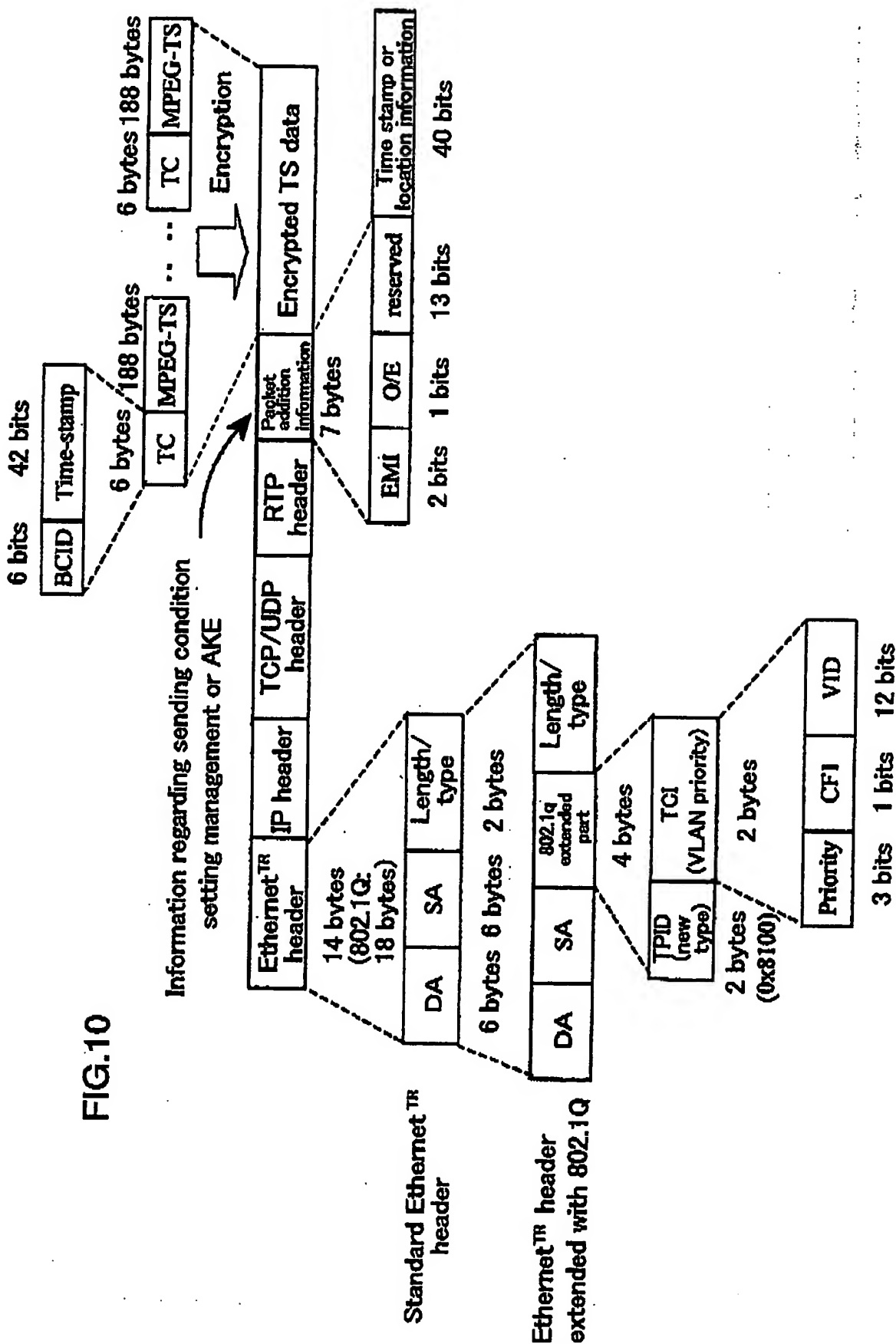


FIG.11

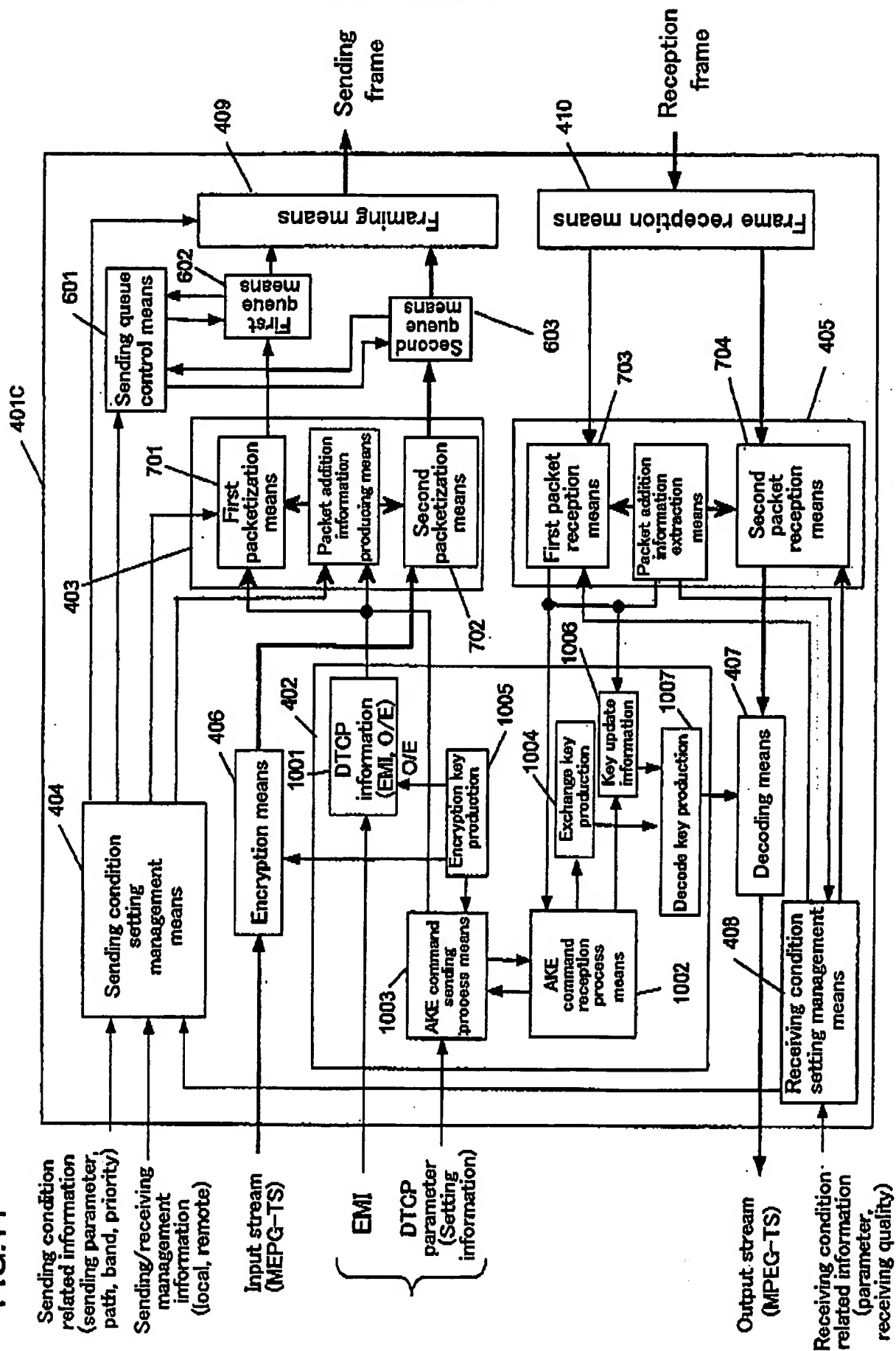


FIG.12

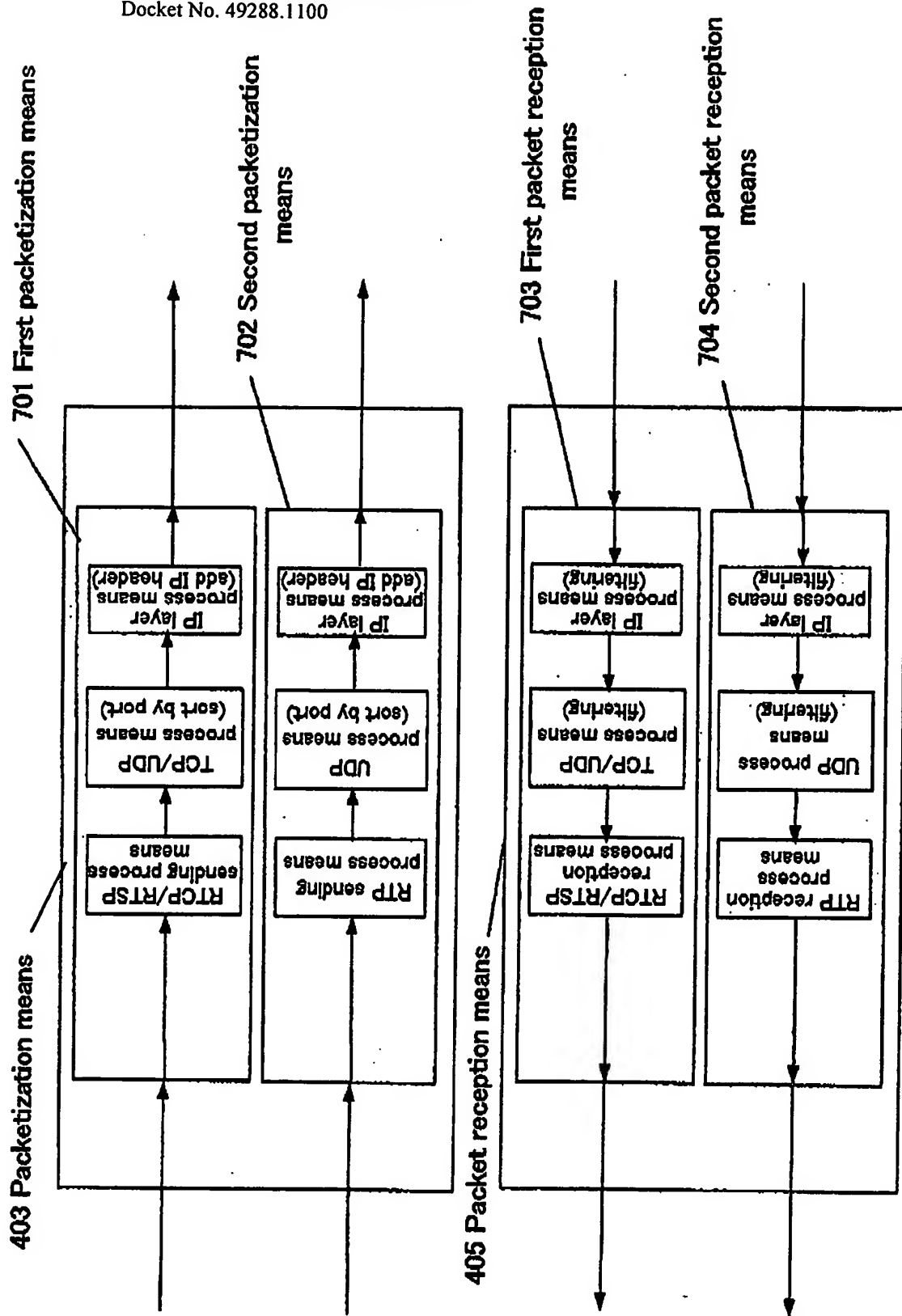


FIG.13

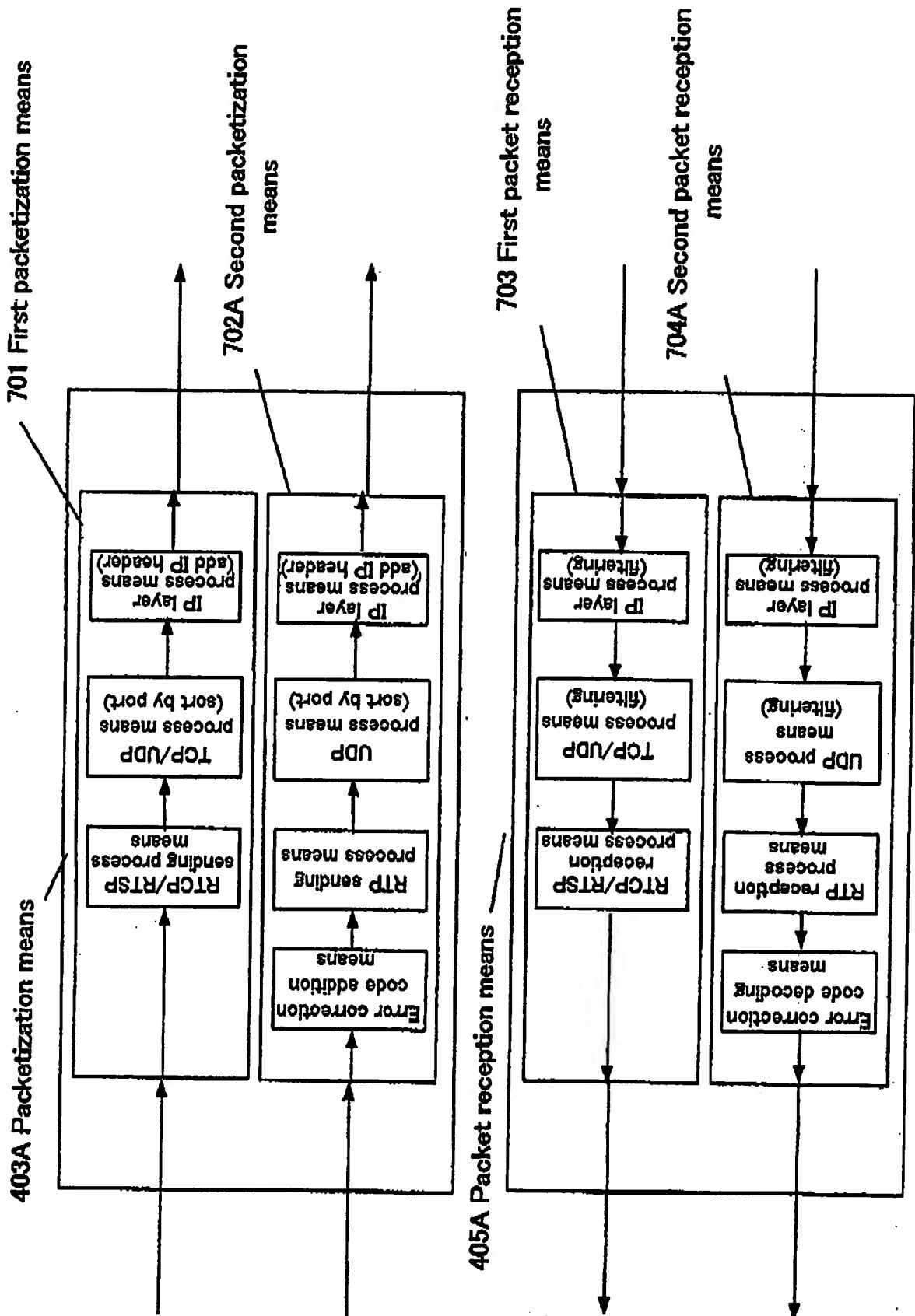
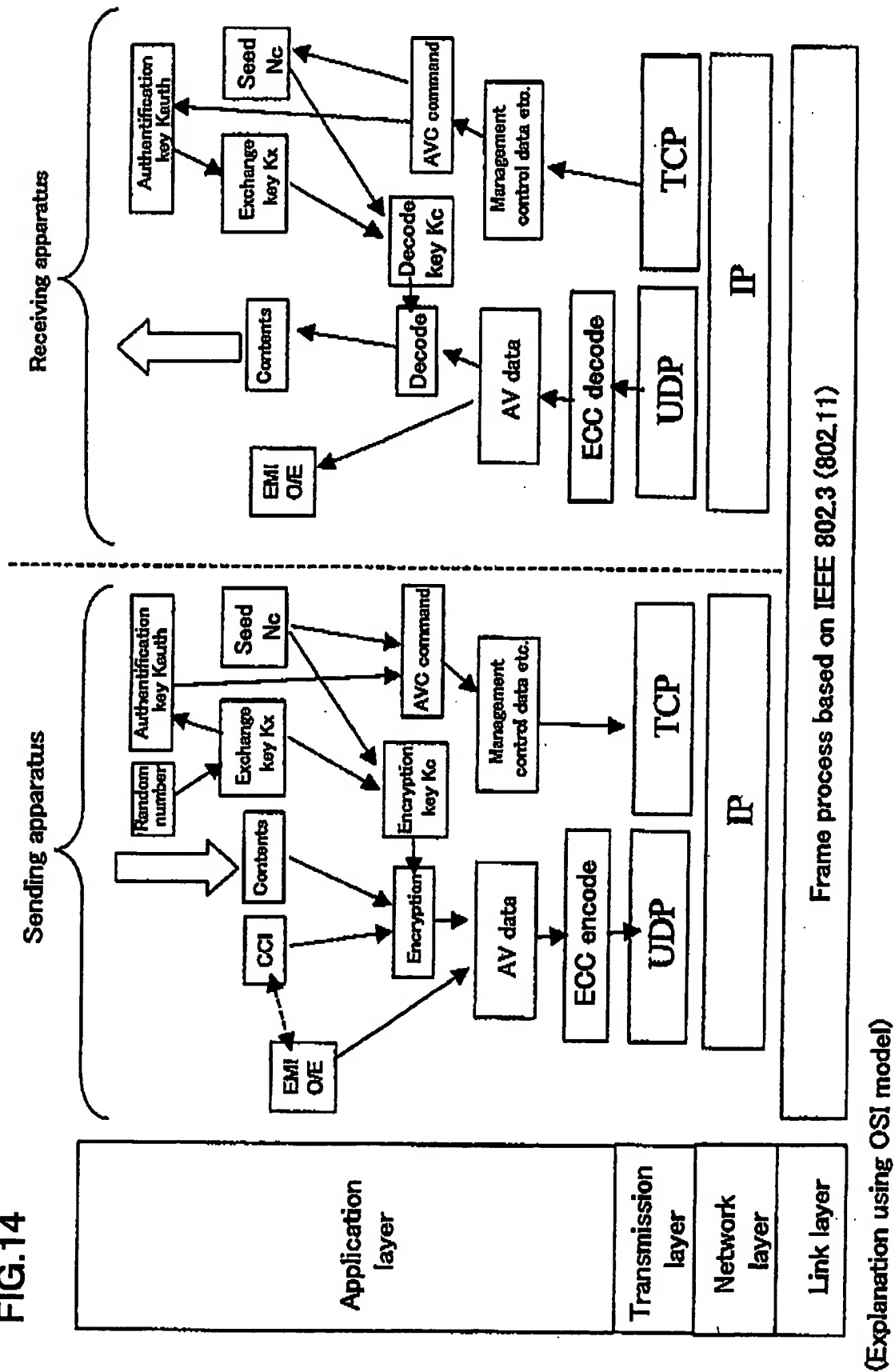
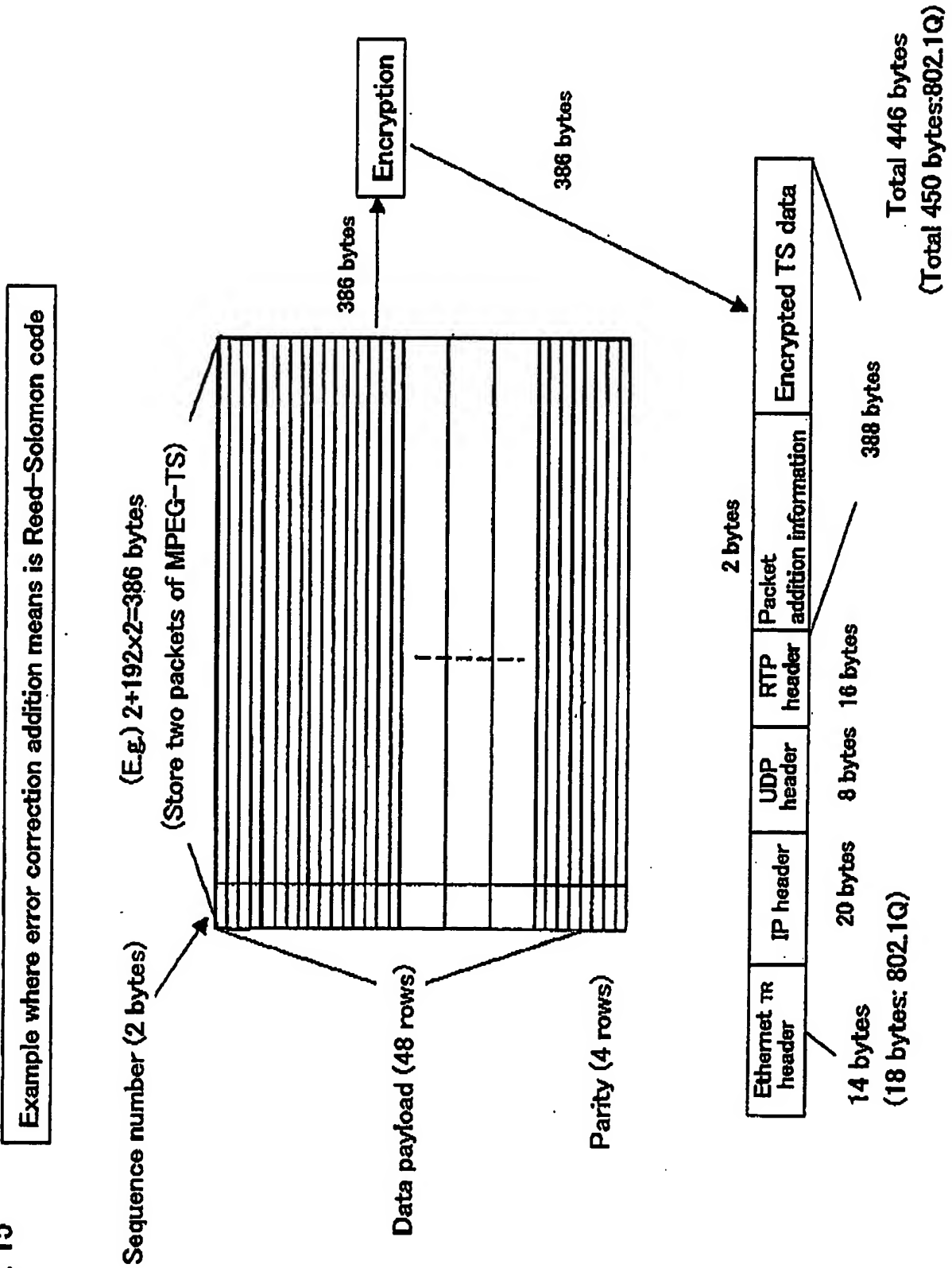


FIG.14



(Explanation using OSI model)

Fig. 15



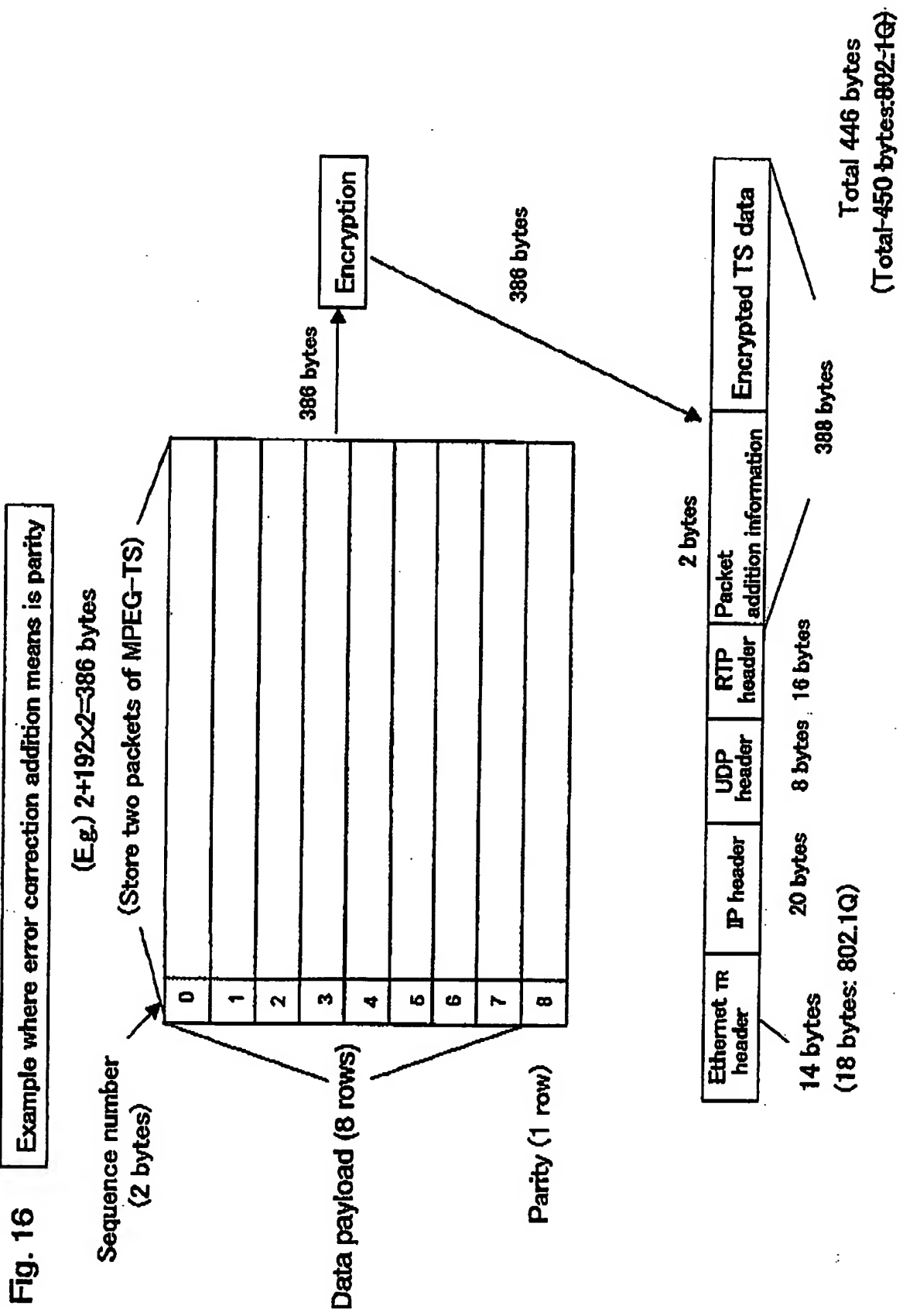




FIG.17

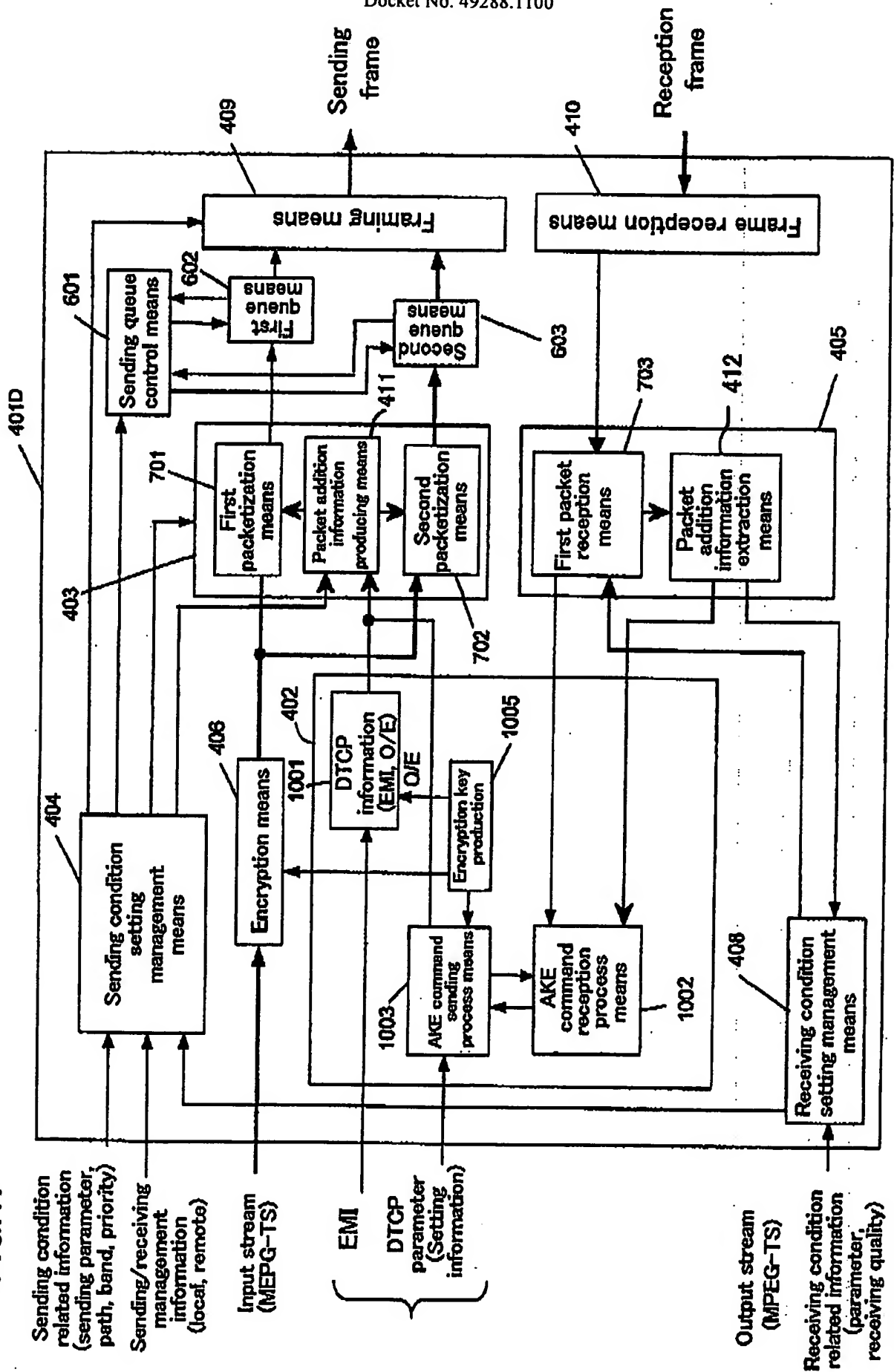


FIG.18

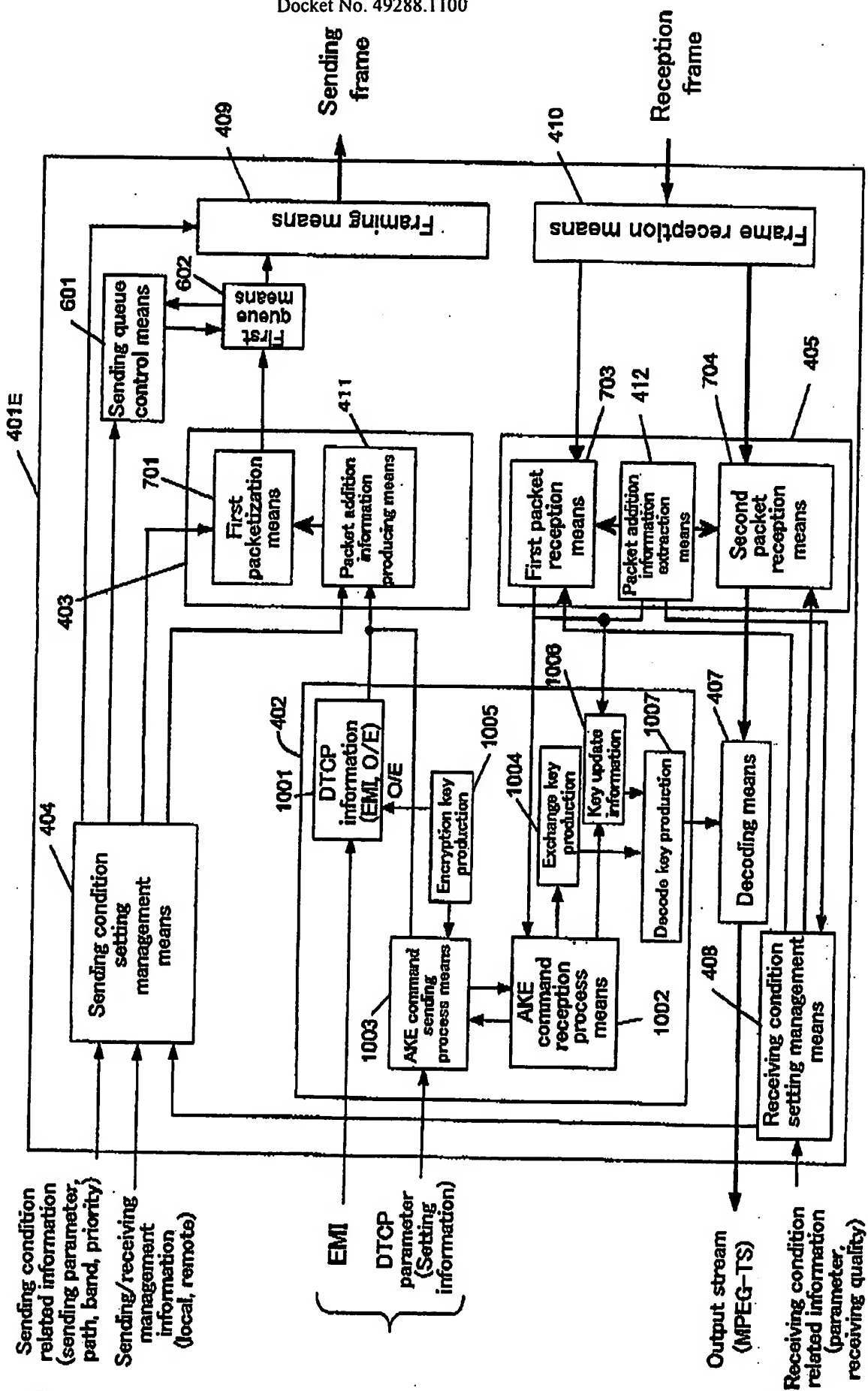


Fig. 19

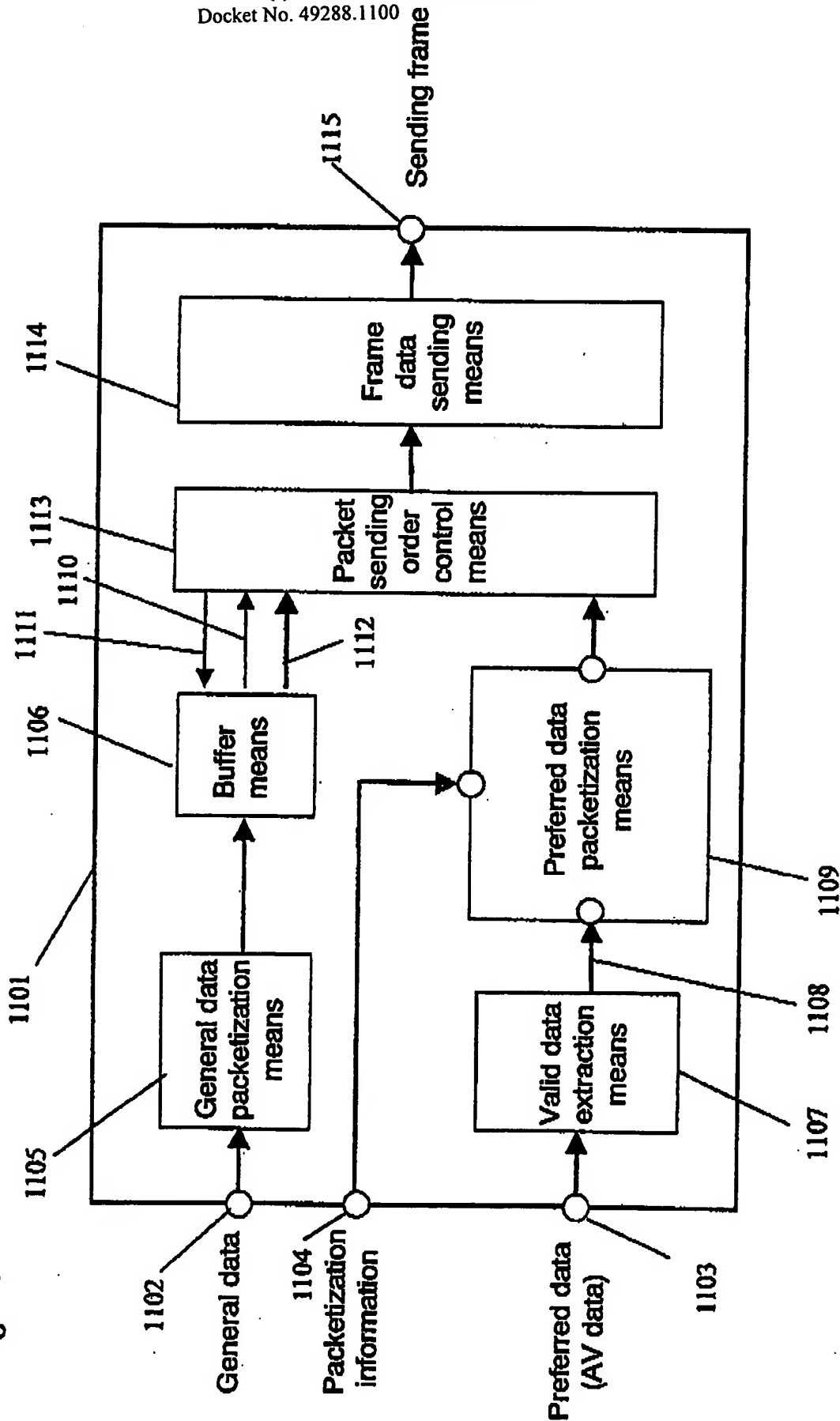
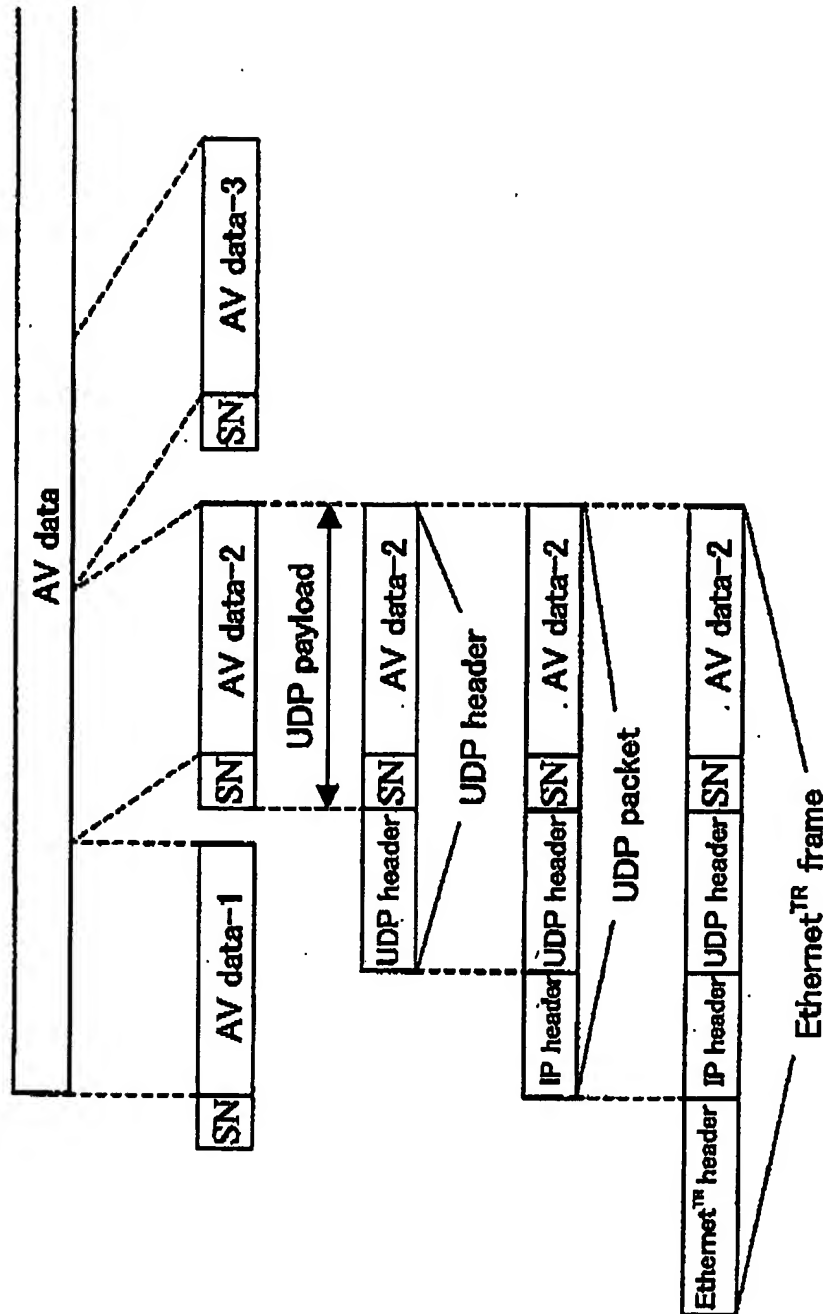
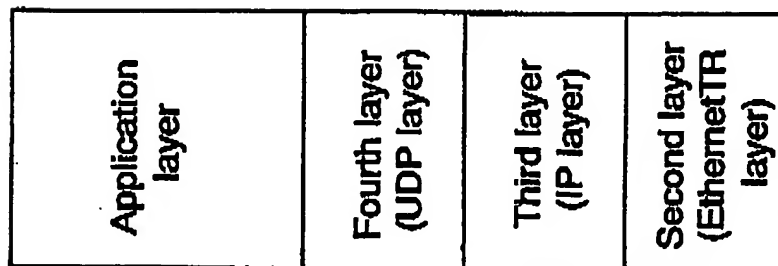


FIG.20



(Representation using OSI model)

FIG.21

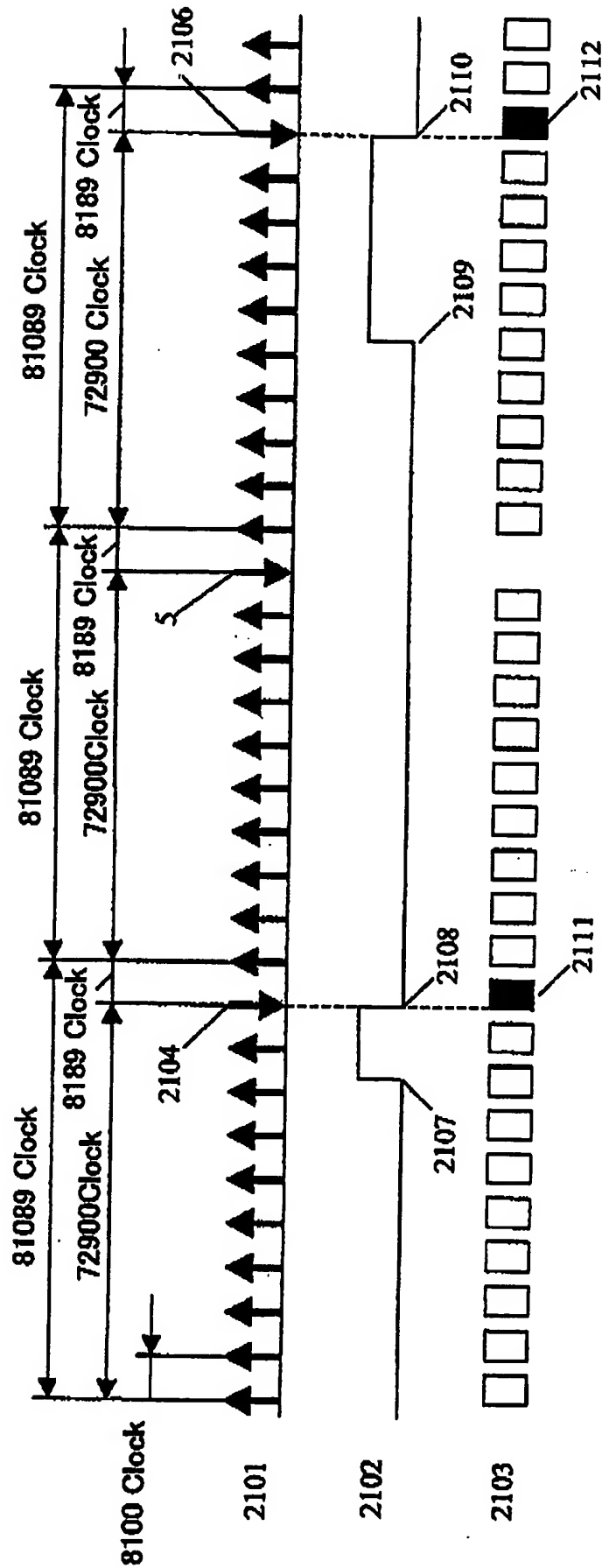


FIG.22

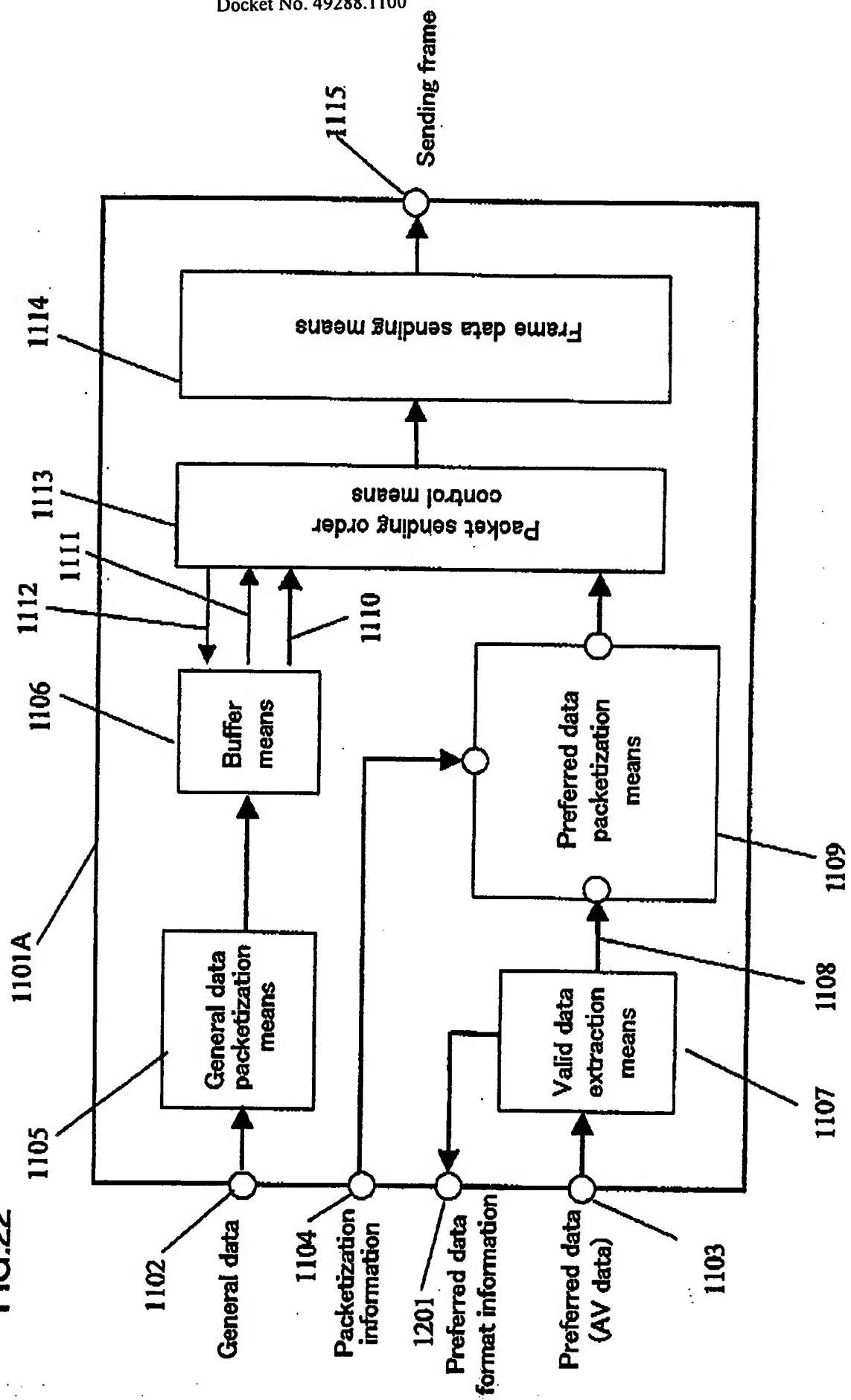


FIG.23

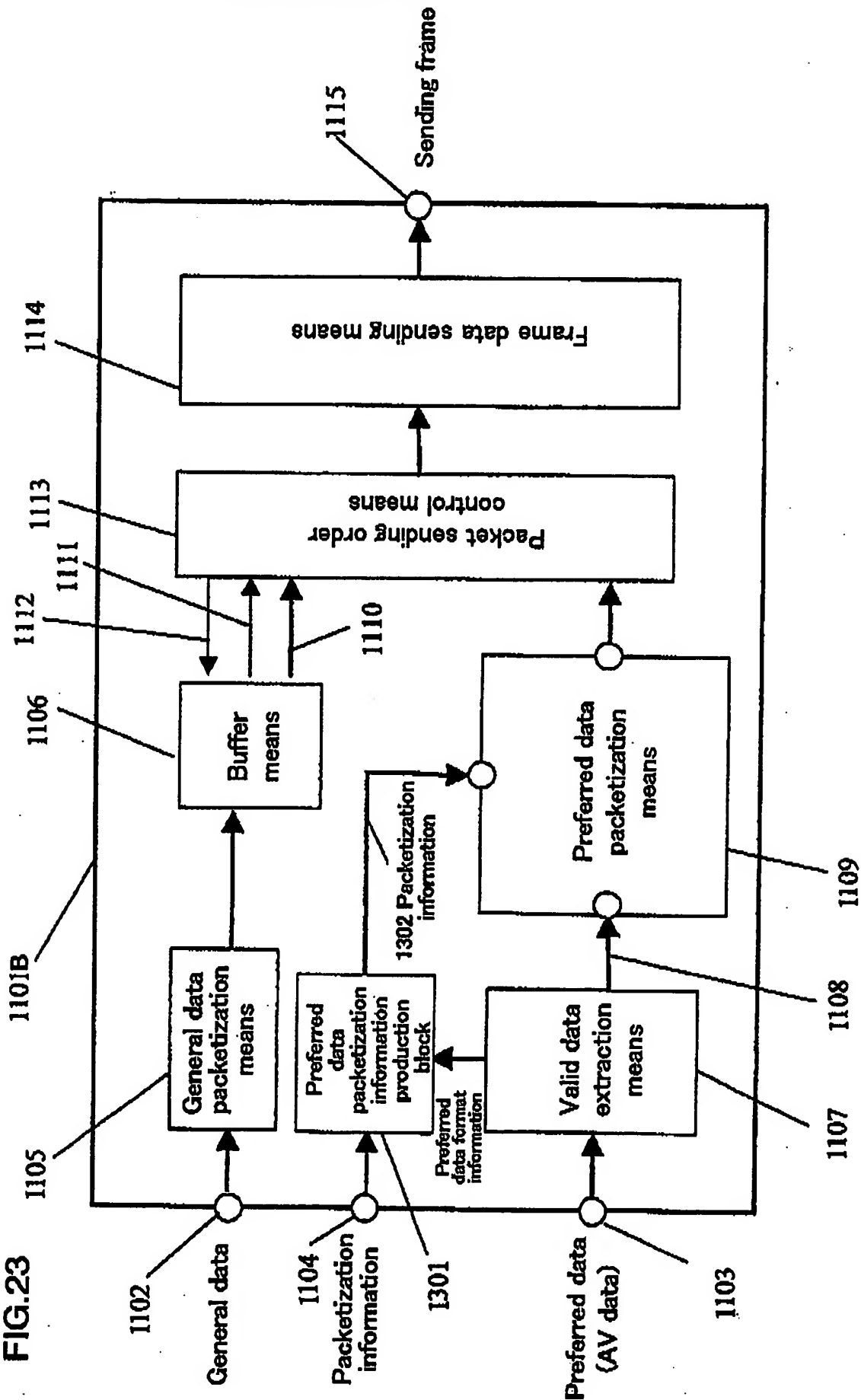


FIG. 24

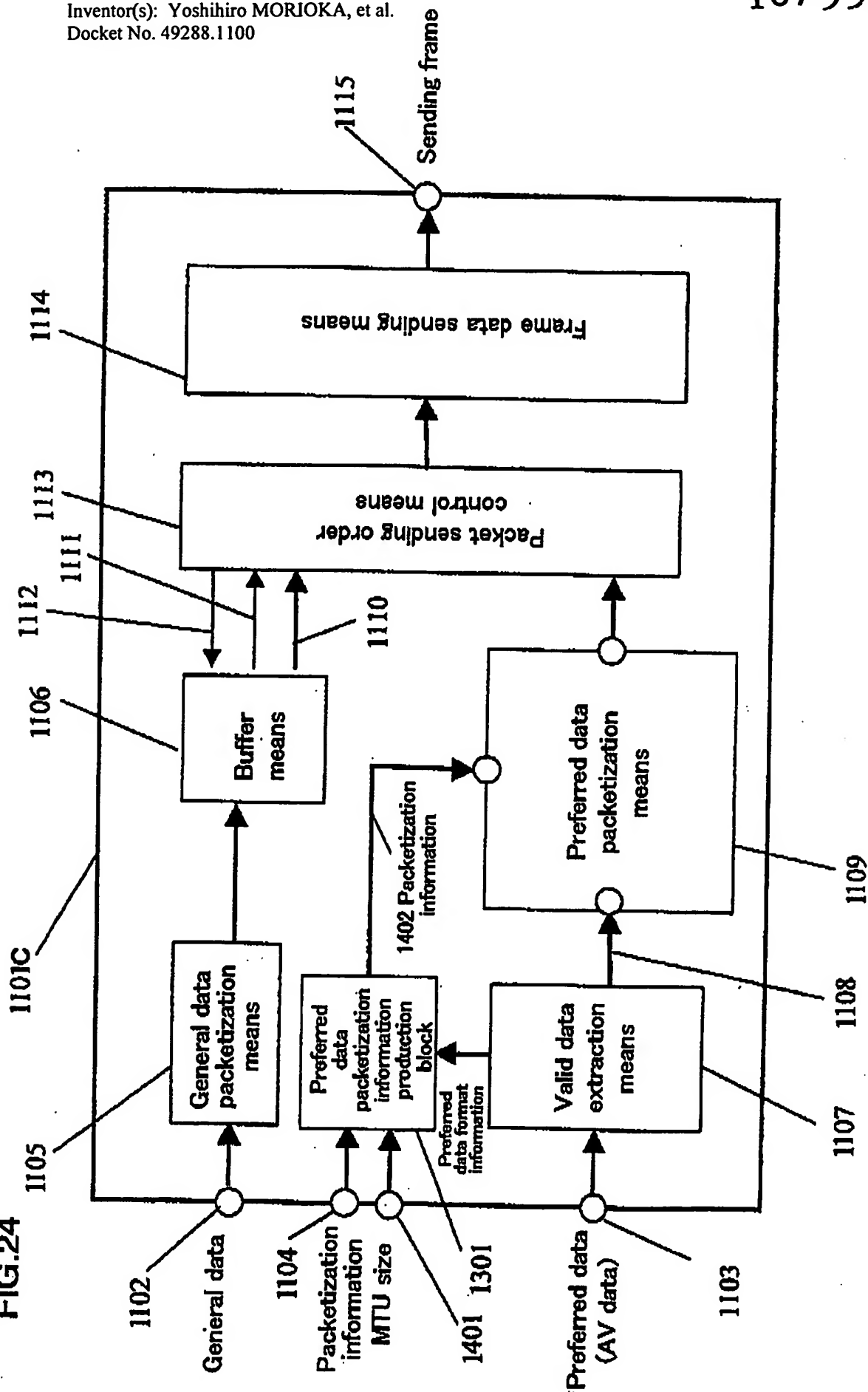
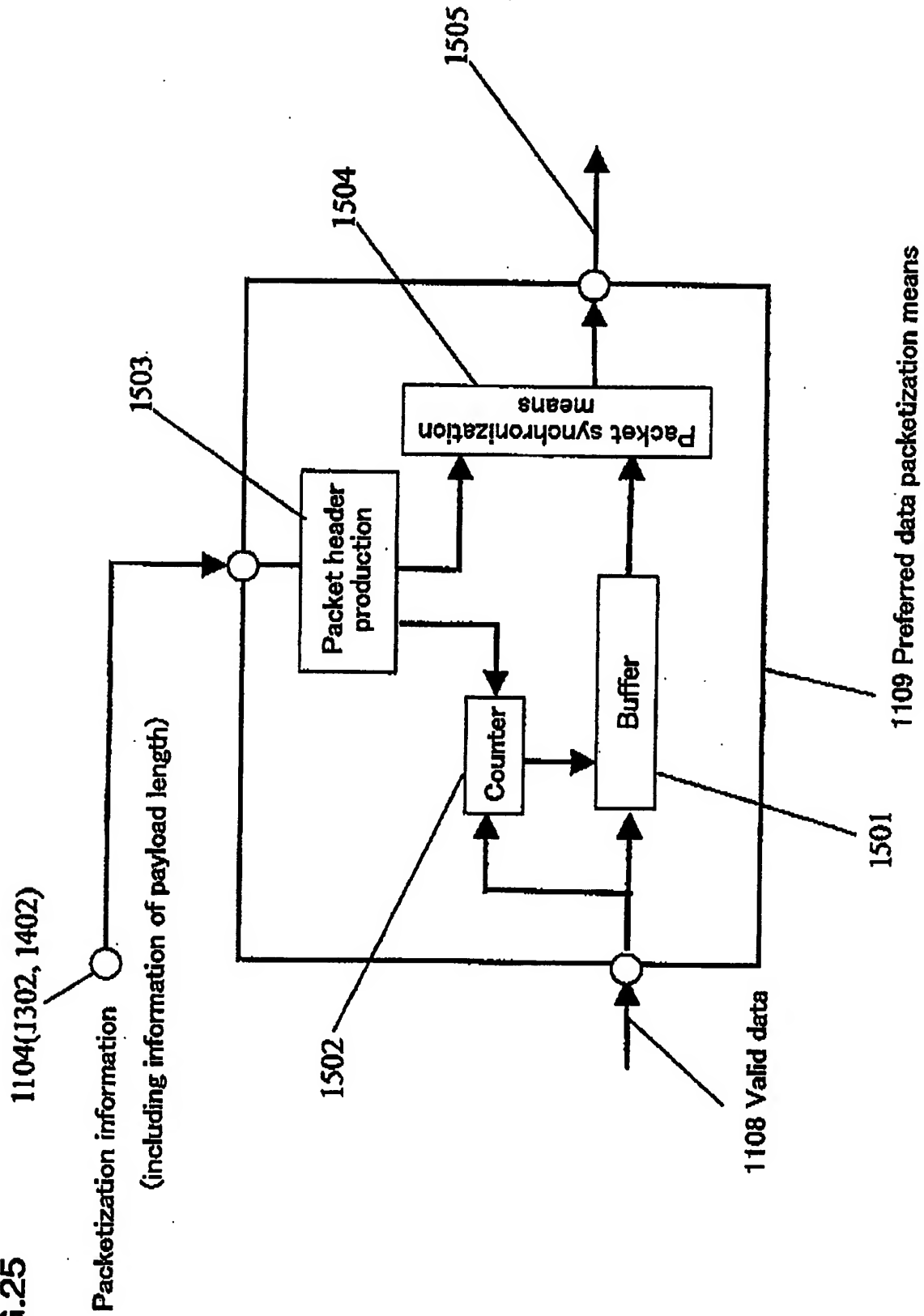




FIG.25



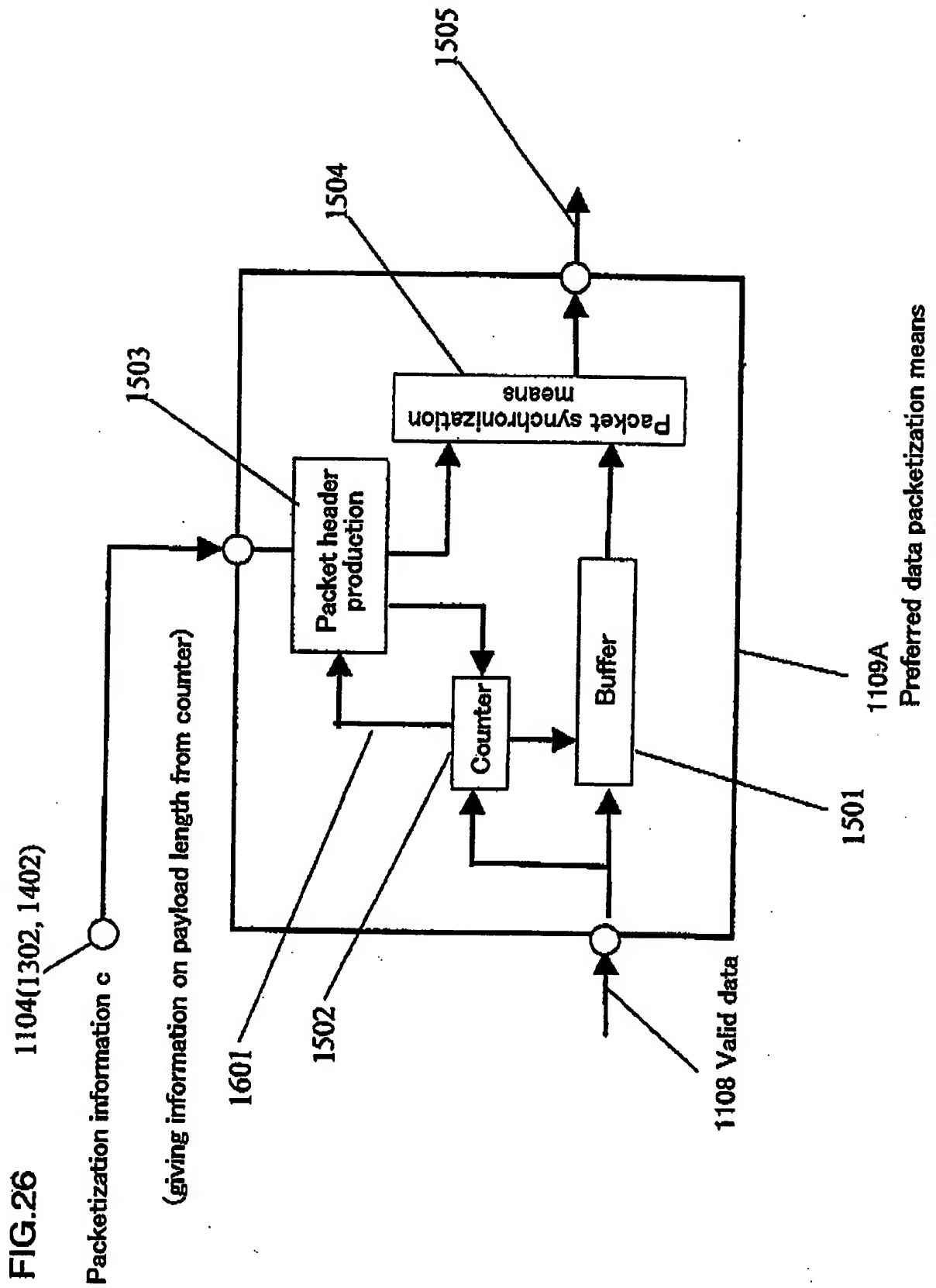


FIG. 27

1104(1302, 1402)

Packetization information

1503

1601

1502

Packet header production

1505

1504

Packet synchronization means

Error correction addition means

1701

1109B

Preferred data packetization means

1501

1108 Valid data

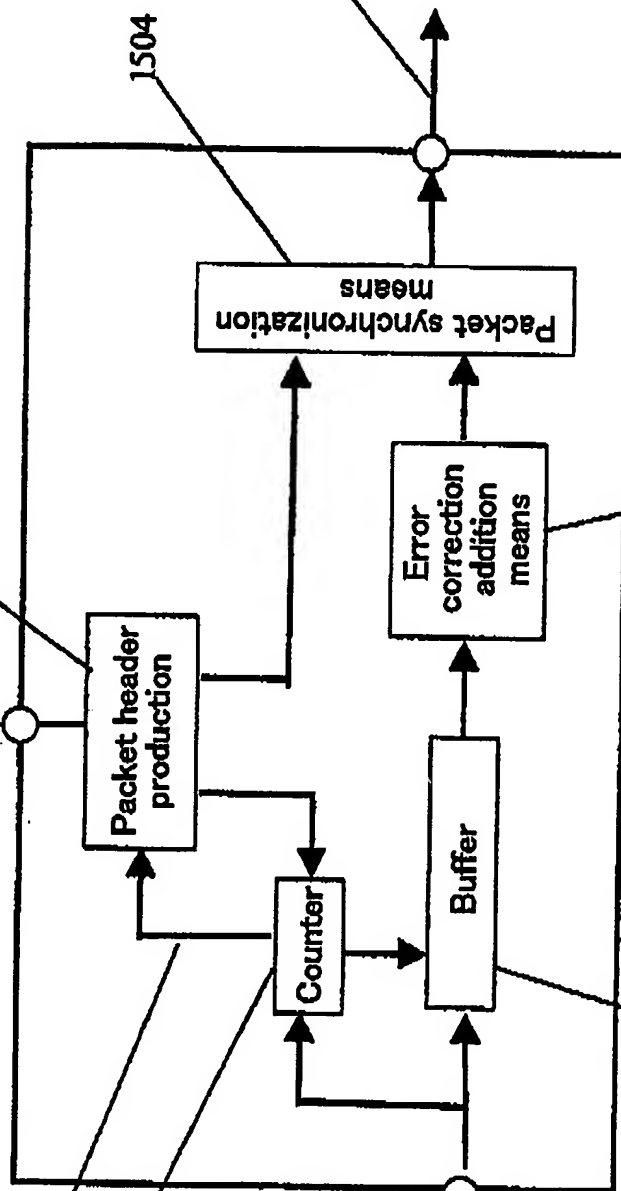


Fig. 28

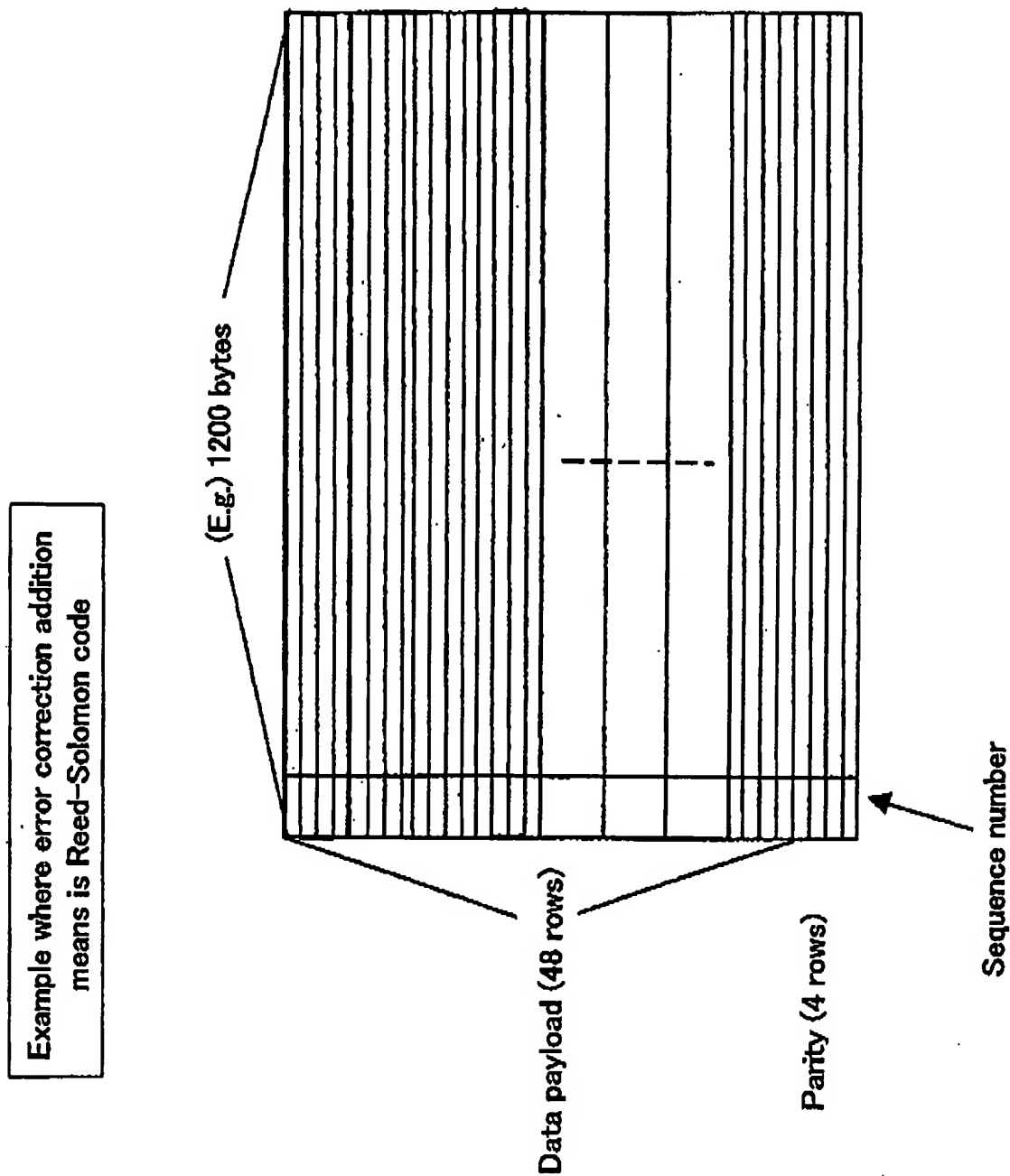


Fig. 29

Example where error correction addition means is parity

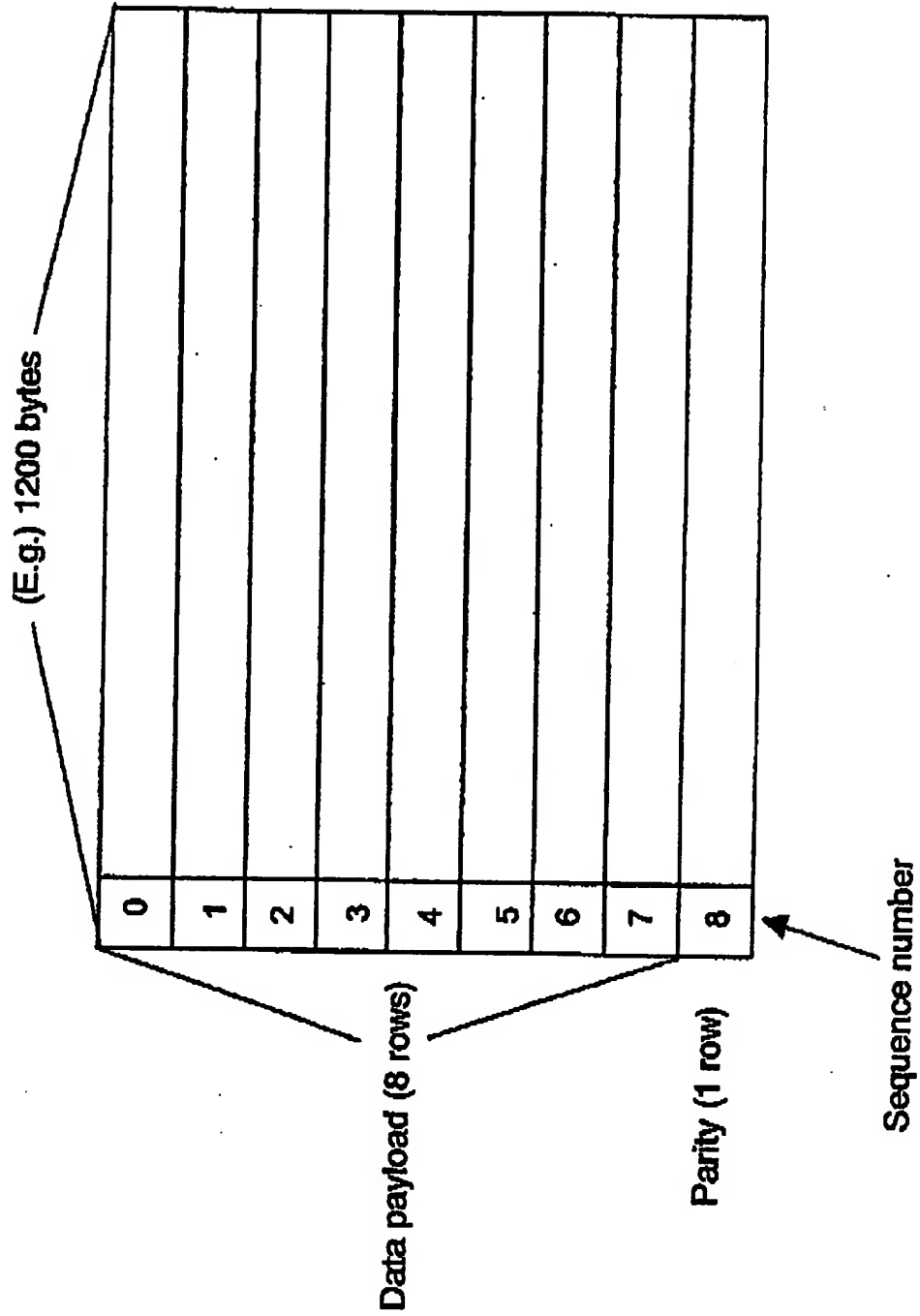
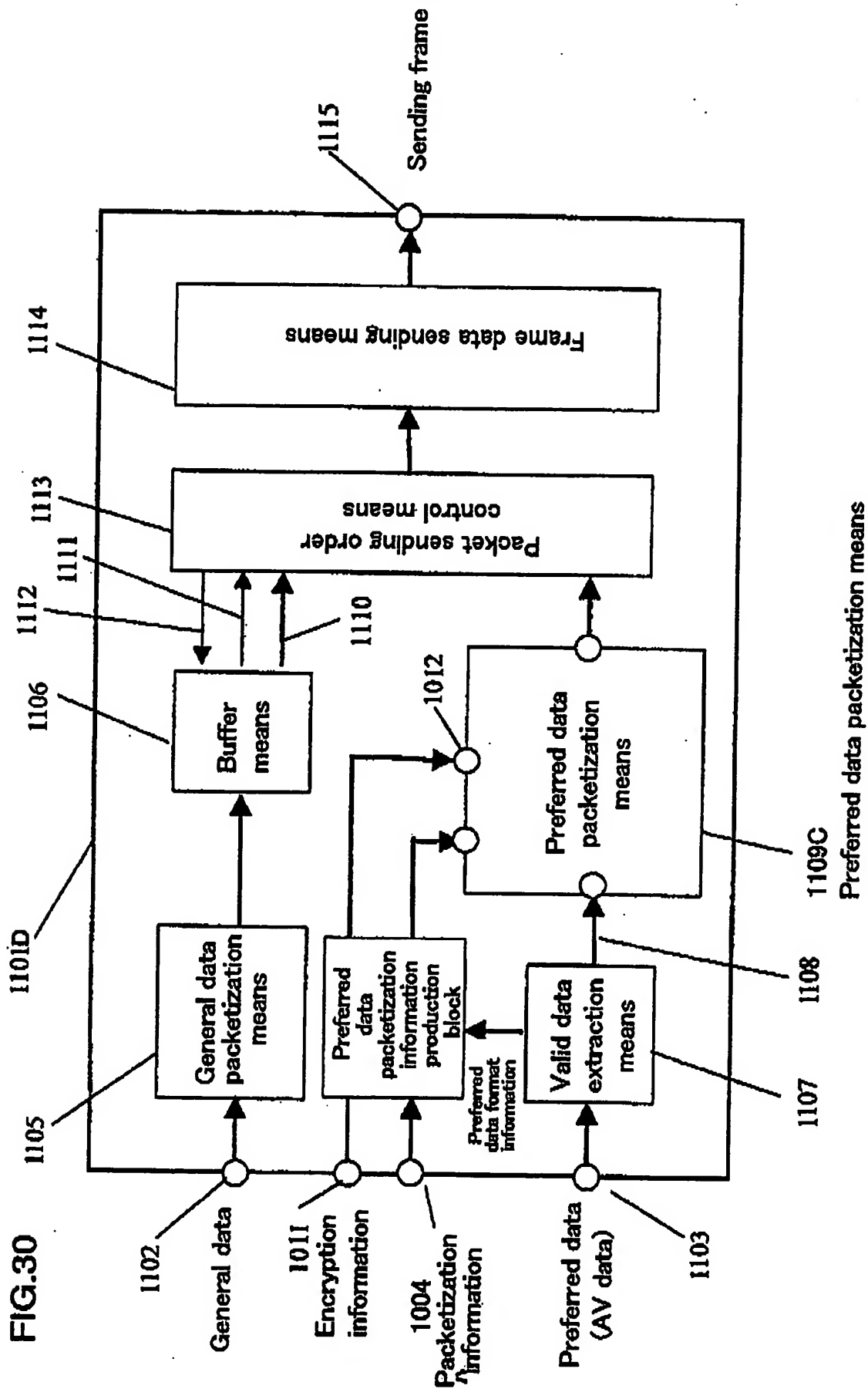
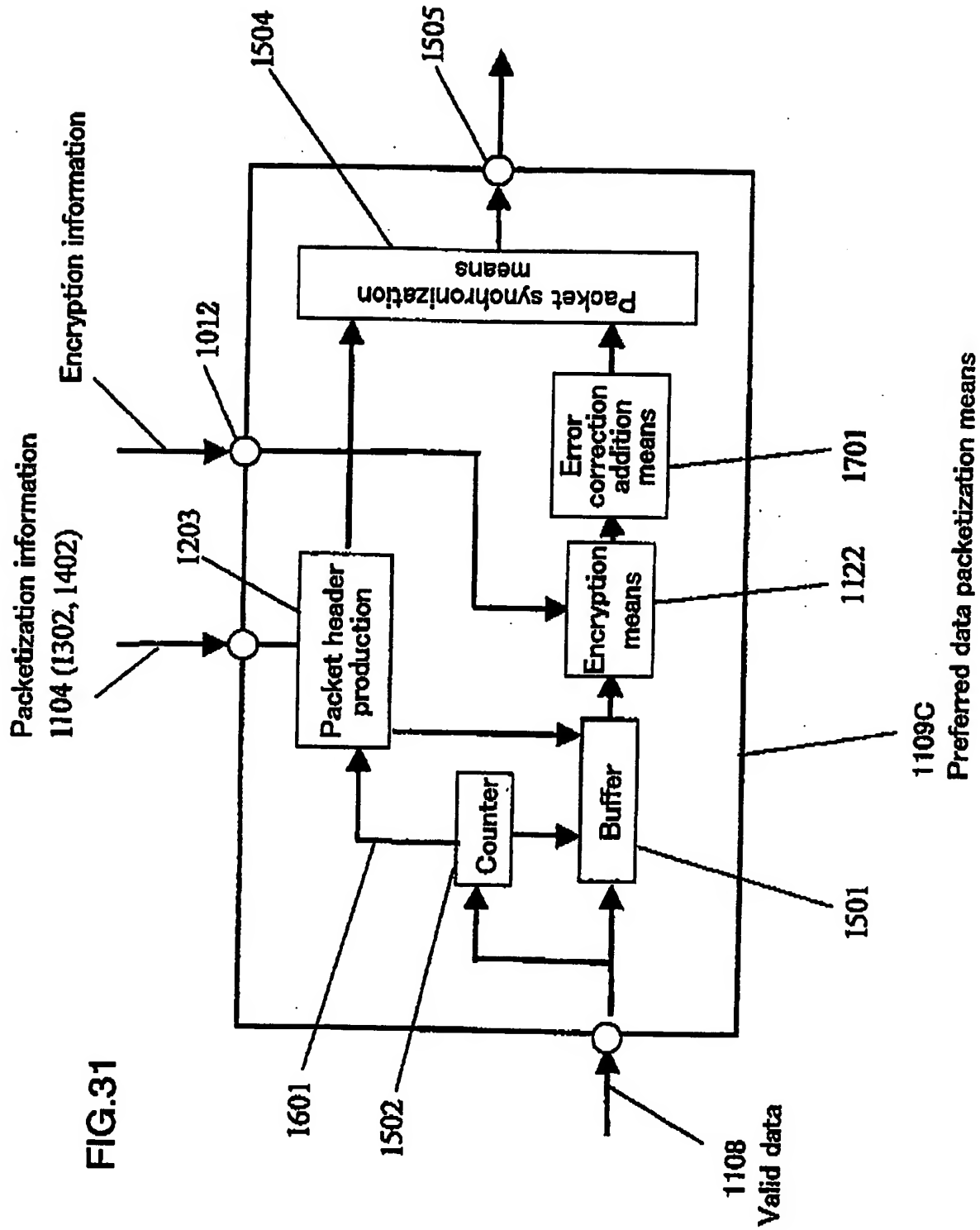


FIG.30





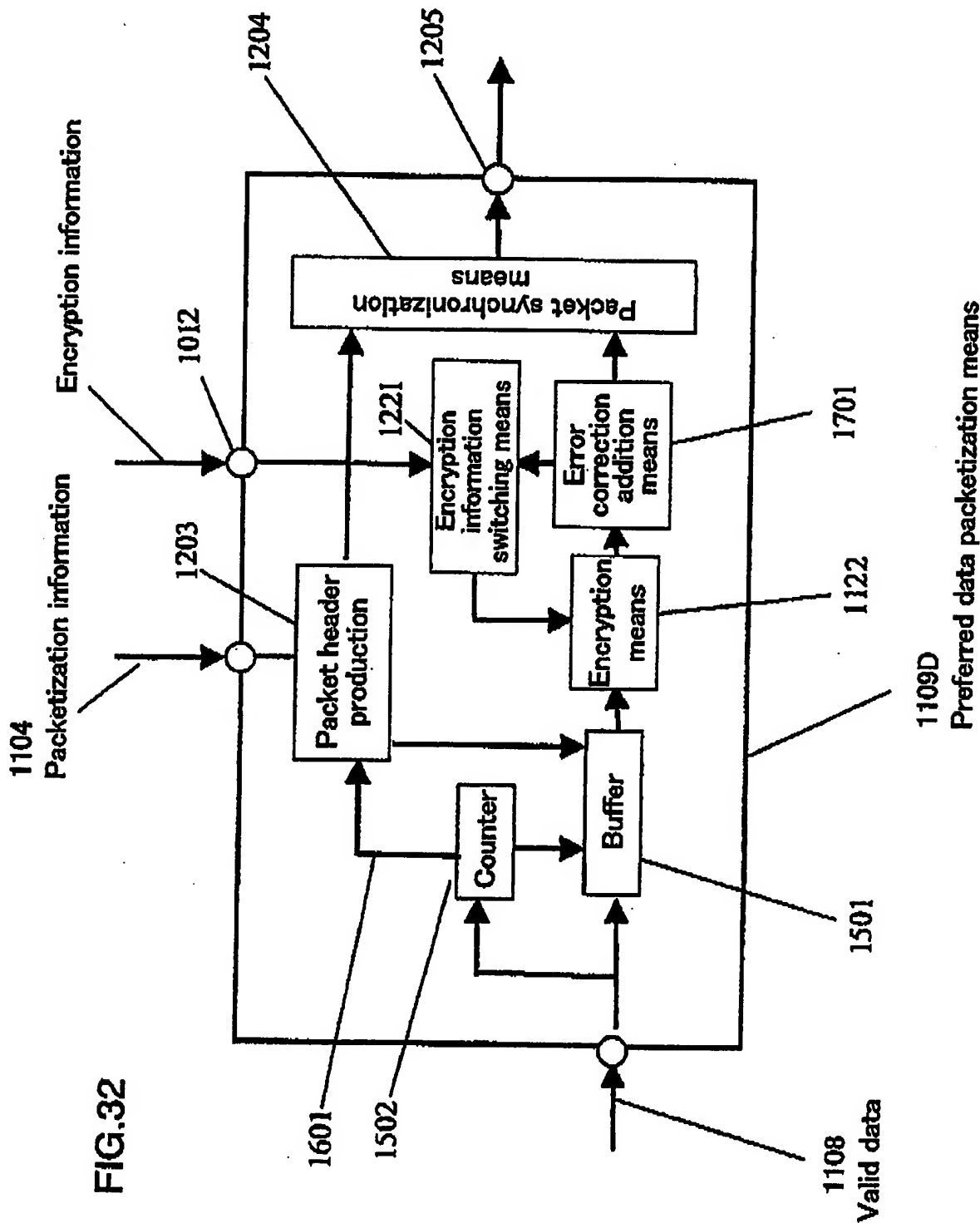
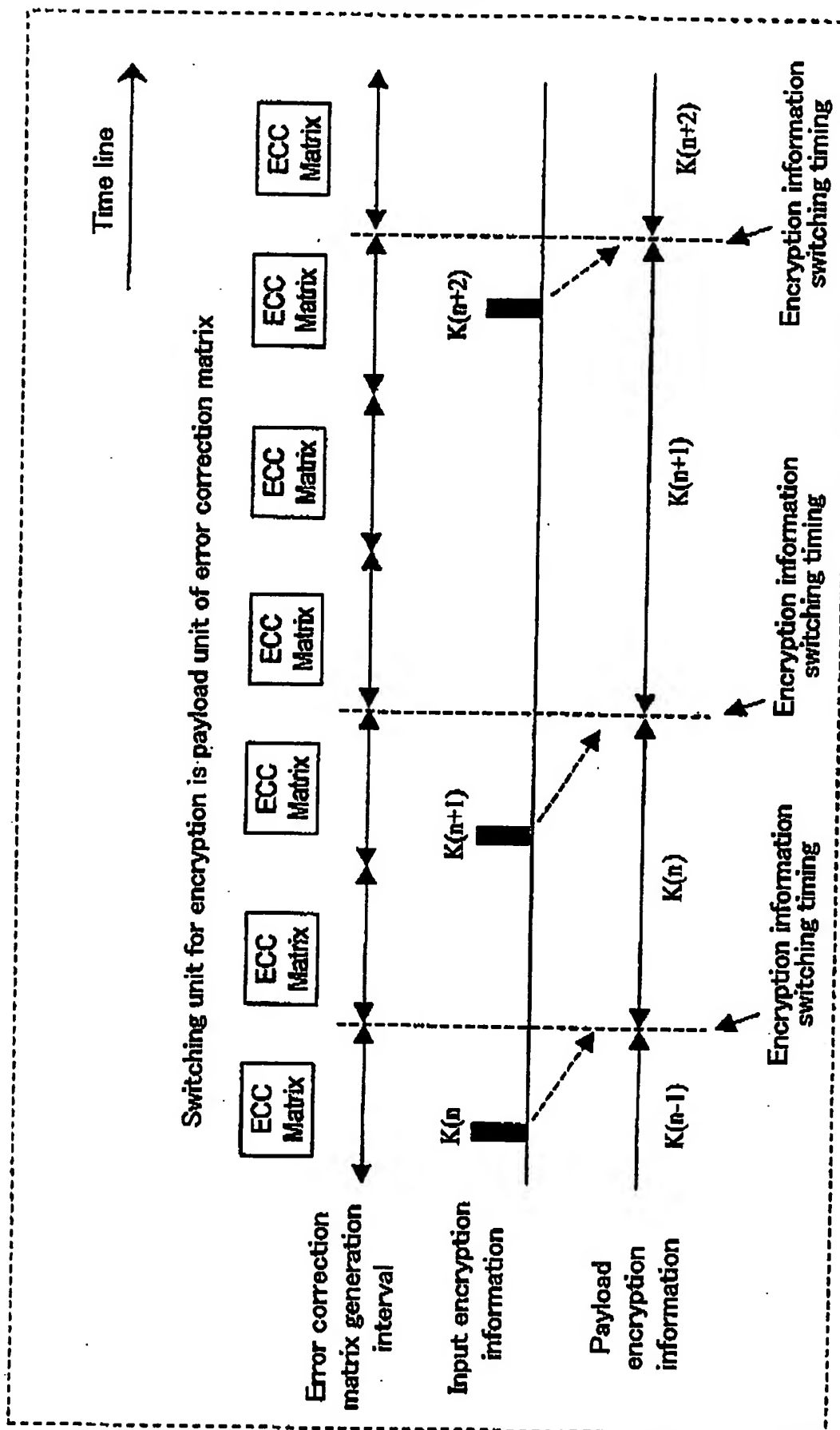




FIG.33



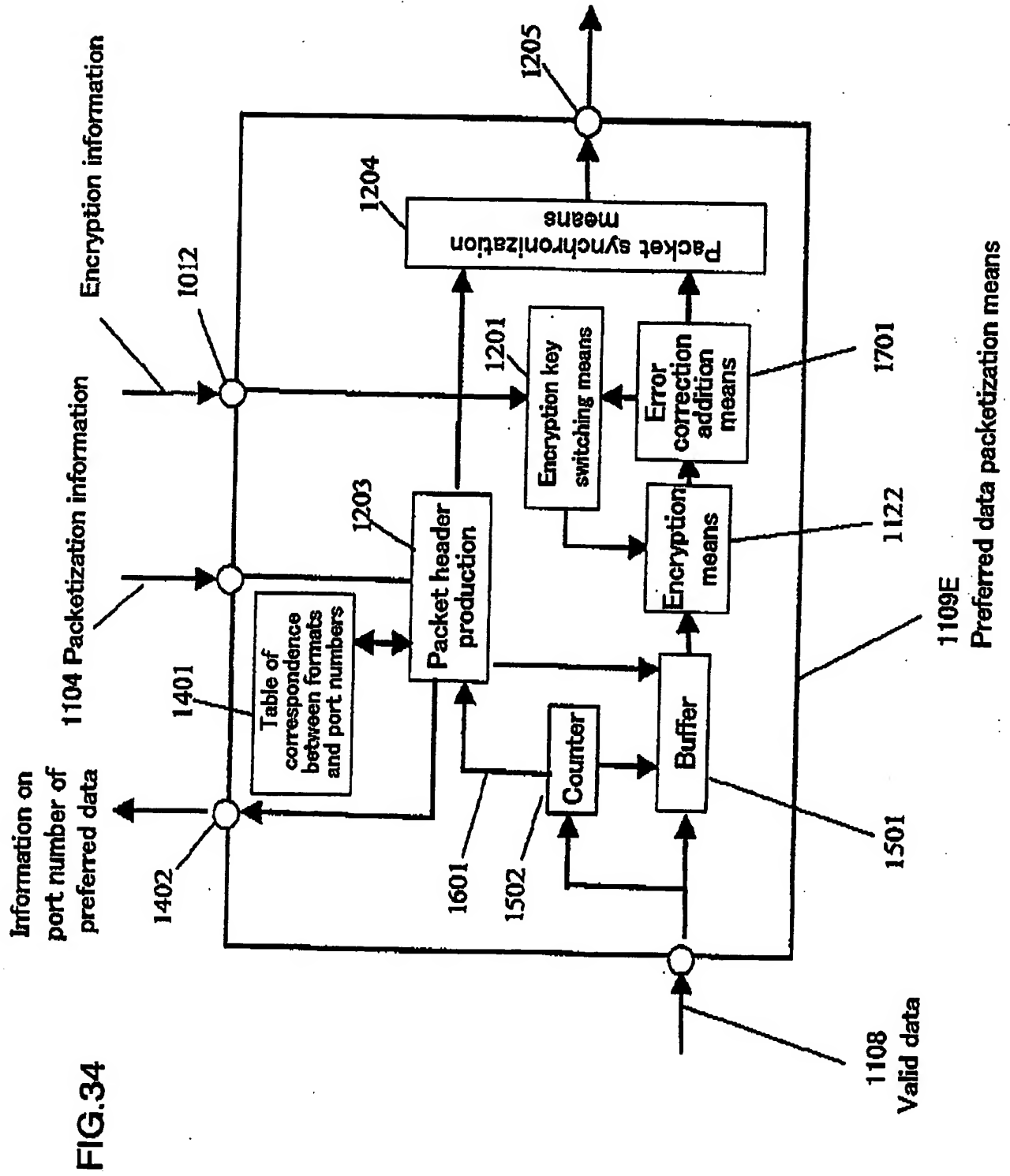




FIG.36

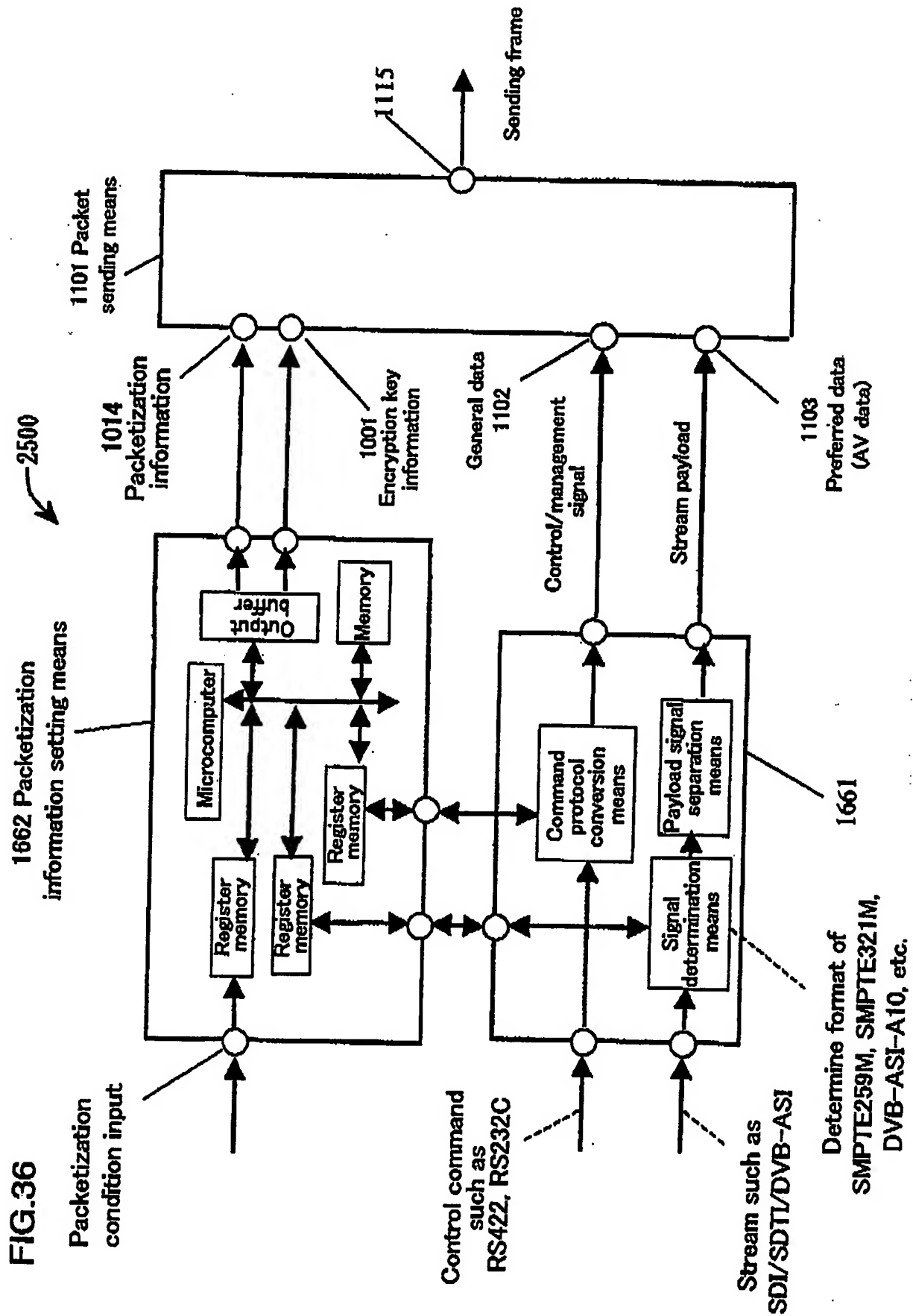


FIG.37

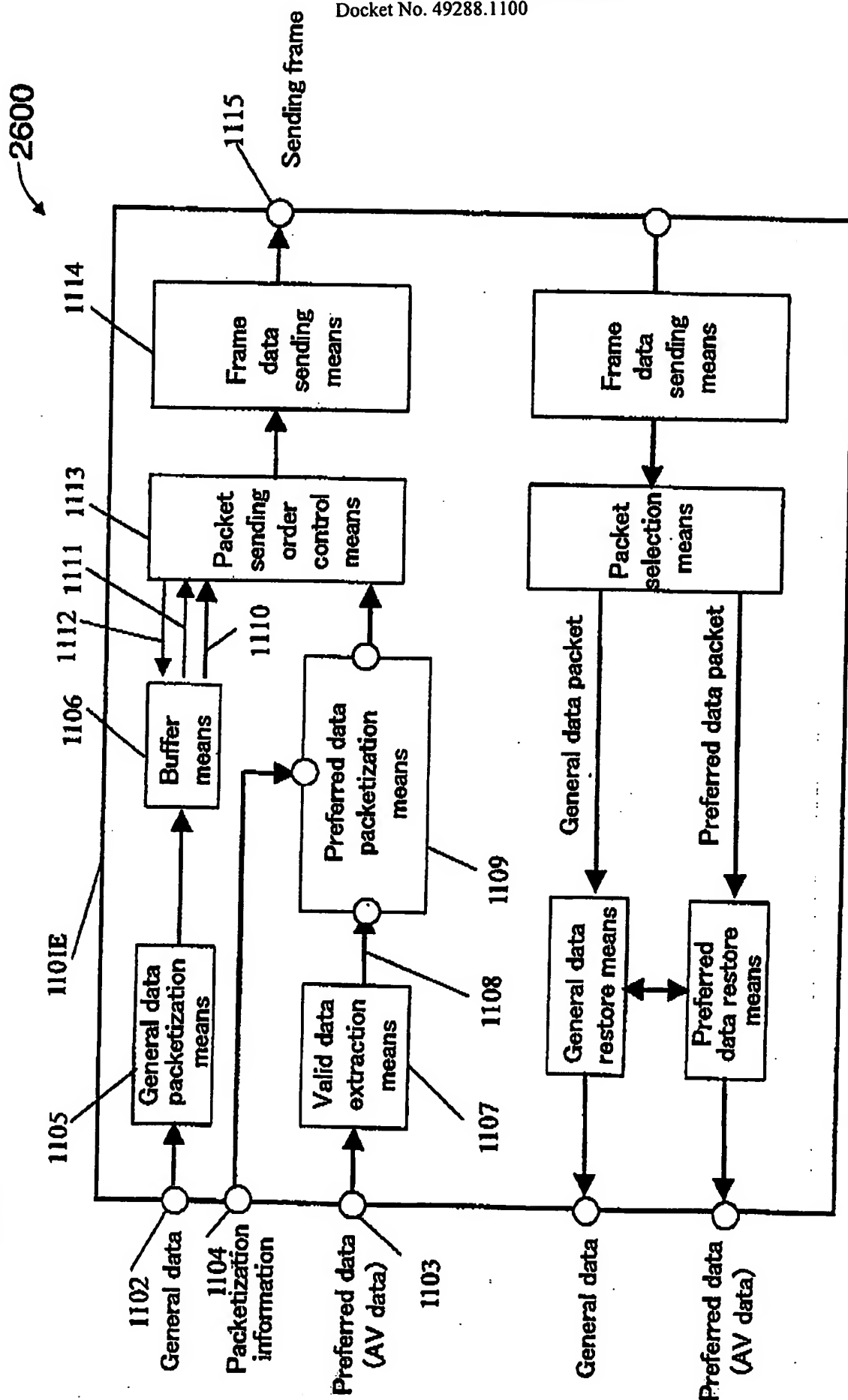
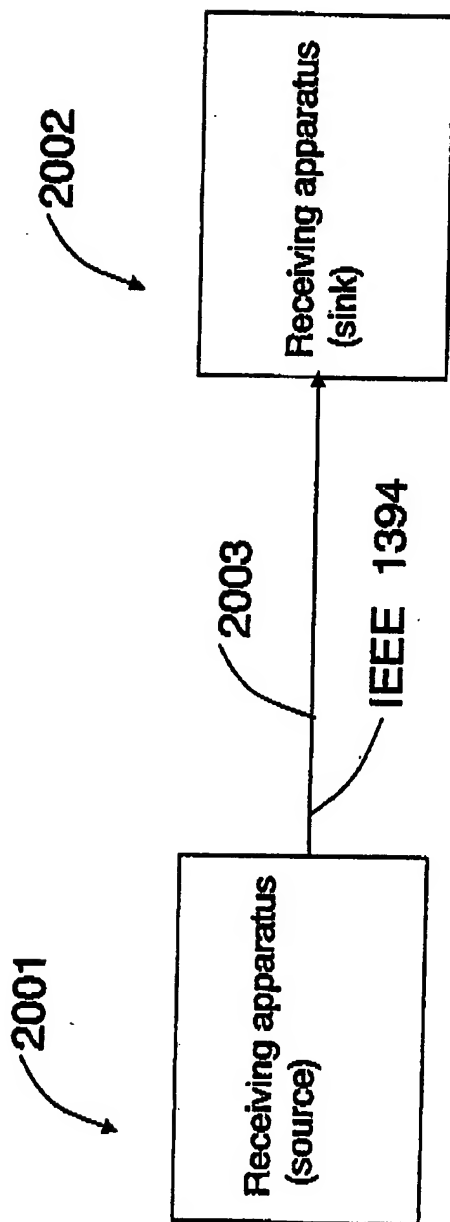


Fig. 38



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